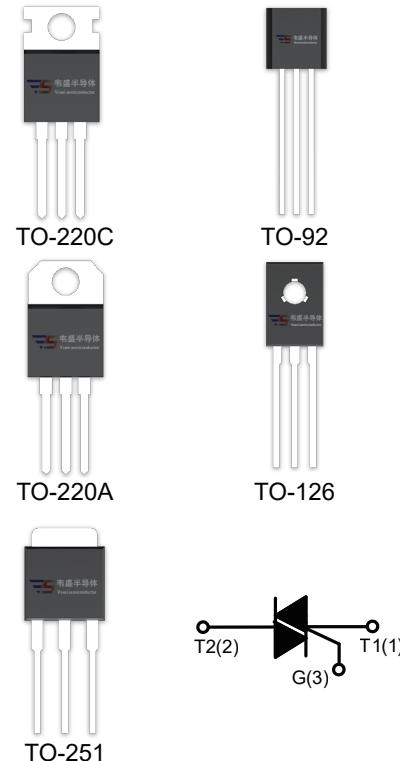


## DESCRIPTION:

The BT134-800E SCR series with the parallel resistor between Gate and Cathode are especially recommended for use on straight hair, igniter, anion generator, etc.



## MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	4	A
$V_{DRM}/V_{RRM}$	600/800	V

## ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	$T_{stg}$	-40 - 150	°C
Operating junction temperature range	$T_j$	-40 - 125	°C
Repetitive peak off-state voltage( $T_j = 25^\circ\text{C}$ )	$V_{DRM}$	600/800	V
Repetitive peak reverse voltage( $T_j = 25^\circ\text{C}$ )	$V_{RRM}$	600/800	V
Non repetitive surge peak Off-state voltage	$V_{DSM}$	$V_{DRM} + 100$	V
Non repetitive peak reverse voltage	$V_{RSM}$	$V_{RRM} + 100$	V
RMS on-state current	$I_{T(RMS)}$	4	A
	TO-251 ( $T_c=100^\circ\text{C}$ )		
	TO-220A(Non-Ins)/ TO-220C( $T_c=103^\circ\text{C}$ )		
	TO-202-3 ( $T_c=95^\circ\text{C}$ )		
	SOT-82 /TO-126 ( $T_c=97^\circ\text{C}$ )		
	TO-92 ( $T_c=50^\circ\text{C}$ )		

Non repetitive surge peak on-state current (full cycle, F=50Hz)	I <sub>TSM</sub>	25	A
I <sup>2</sup> t value for fusing (tp =10ms)	I <sup>2</sup> t	3.1	A <sup>2</sup> s
Critical rate of rise of on-state current (I <sub>G</sub> =2×I <sub>GT</sub> )	I - II - III	dI/dt	50
	IV		10
Critical rate of rise of on-state current (I <sub>G</sub> =2×I <sub>GT</sub> )		dI/dt	A/μs
Peak gate current	I <sub>GM</sub>	2	A
Average gate power dissipation	P <sub>G(AV)</sub>	0.5	W
Peak gate power	P <sub>GM</sub>	5	W

**ELECTRICAL CHARACTERISTICS (T<sub>j</sub>=25°C unless otherwise specified)**

Symbol	Test Condition	Quadrant		Value			Unit
				T	D	E	
I <sub>GT</sub>	V <sub>D</sub> =12V R <sub>L</sub> =33Ω	I - II - III	MAX	5	5	10	mA
		IV		5	10	25	
V <sub>GT</sub>		ALL	MAX	1.3			V
V <sub>GD</sub>	V <sub>D</sub> =V <sub>DRM</sub> T <sub>j</sub> =125°C R <sub>L</sub> =3.3KΩ	ALL	MIN	0.2			V
I <sub>L</sub>	I <sub>G</sub> =1.2I <sub>GT</sub>	I - III - IV	MAX	8	10	20	mA
		II		12	15	35	
I <sub>H</sub>	I <sub>T</sub> =100mA	MAX		5	10	20	mA
dV/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open T <sub>j</sub> =125°C	MIN		20	50	100	V/μs
(dV/dt)c	(dI/dt)c=1.1A/ms T <sub>j</sub> =125°C	MIN		0.5	1	5	V/μs

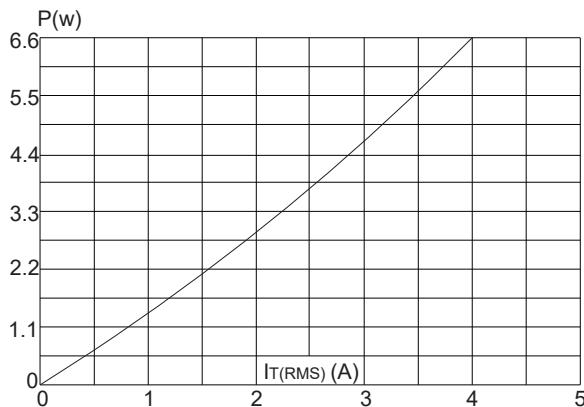
**STATIC CHARACTERISTICS**

Symbol	Parameter		Value(MAX)	Unit
V <sub>TM</sub>	I <sub>TM</sub> =5A tp=380μs	T <sub>j</sub> =25°C	1.7	V
I <sub>DRM</sub>	V <sub>D</sub> =V <sub>DRM</sub> V <sub>R</sub> =V <sub>RRM</sub>	T <sub>j</sub> =25°C	5	μA
I <sub>RRM</sub>		T <sub>j</sub> =125°C	0.5	mA

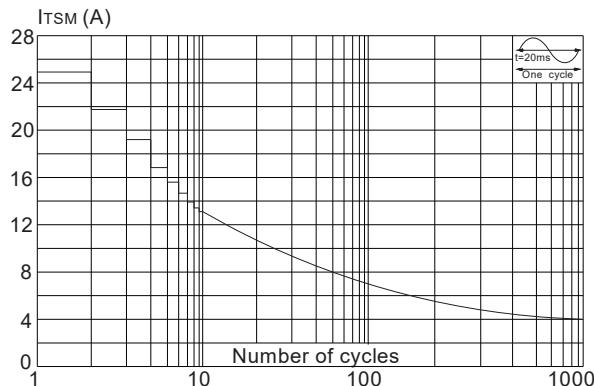
**THERMAL RESISTANCES**

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case(AC)	TO-251	3.7
		TO-220A(Non-Ins)/ TO-220C	3.1
		TO-202-3	4.5
		SOT-82/TO-126	4.1
		TO-92	11.2

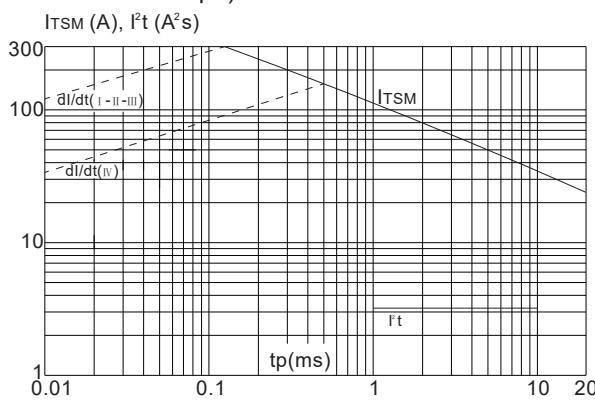
**FIG.1:** Maximum power dissipation versus RMS on-state current



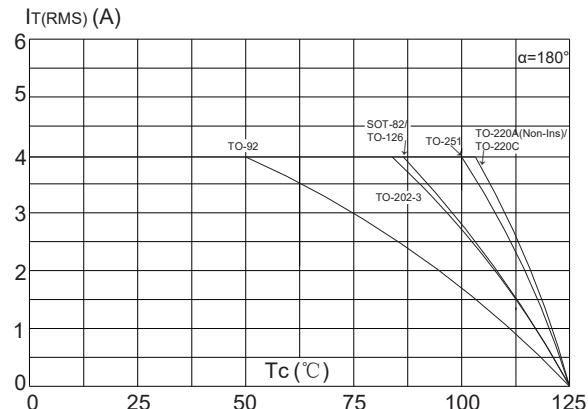
**FIG.3:** Surge peak on-state current versus number of cycles



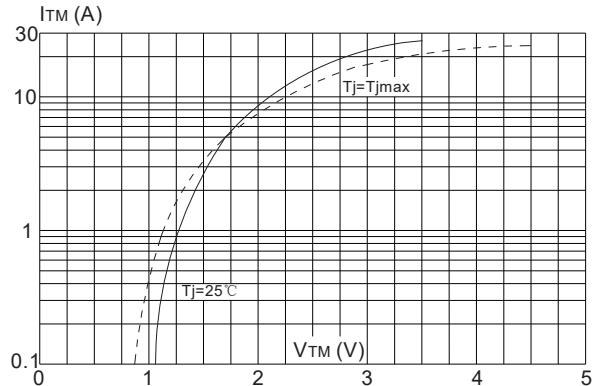
**FIG.5:** Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 20\text{ms}$  and corresponding value of  $I^2t$  ( I - II - III:  $dI/dt < 50\text{A}/\mu\text{s}$ ; IV:  $dI/dt < 10\text{A}/\mu\text{s}$ )



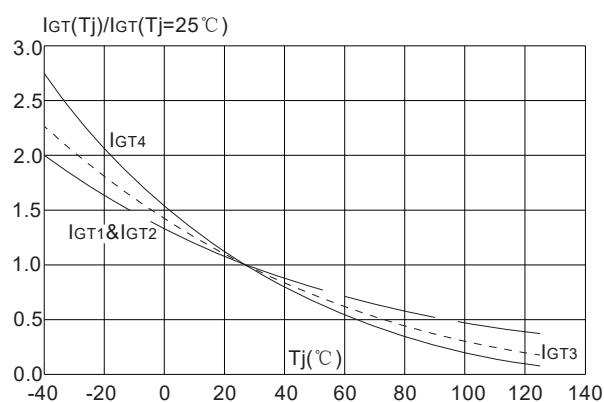
**FIG.2:** RMS on-state current versus case temperature



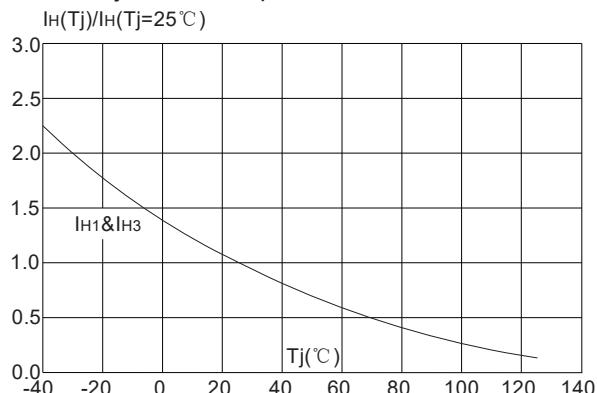
**FIG.4:** On-state characteristics (maximum values)



**FIG.6:** Relative variations of gate trigger current versus junction temperature



**FIG.7:** Relative variations of holding current versus junction temperature



**FIG.8:** Relative variations of latching current versus junction temperature

