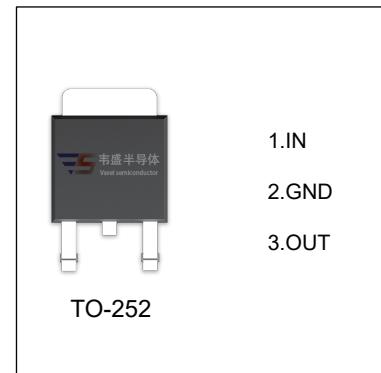


VS78D12 Three-terminal positive voltage regulator

FEATURES

- Maximum output current I_{OM} : 1.0 A
- Output voltage V_O : 12 V
- Continuous total dissipation P_D : 1.25 W ($T_a = 25^\circ C$)



ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

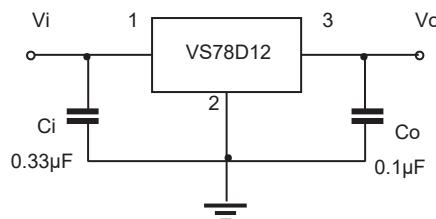
Parameter	Symbol	Value	Unit
Input Voltage	V_i	35	V
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	80	°C/W
Operating Junction Temperature Range	T_{OPR}	-40~+125	°C
Storage Temperature Range	T_{STG}	-65~+150	°C

ELECTRICAL CHARACTERISTICS AT SPECIFIED VIRTUAL JUNCTION TEMPERATURE ($V_i=19V$, $I_o=500mA$, $C_i=0.33\mu F$, $C_o=0.1\mu F$, unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Output Voltage	V_o	$T_J=25^\circ C$	11.64	12.0	12.36	V
		$I_o= 5mA-1A$, $15.5V \leq V_i \leq 27V$	11.4	12.0	12.6	V
Load Regulation	ΔV_o	$I_o=5mA - 1.0A, T_J=25^\circ C$			240	mV
		$I_o=250mA - 750mA, T_J=25^\circ C$			120	mV
Line Regulation	ΔV_o	$14.5V \leq V_i \leq 30V, T_J=25^\circ C$			240	mV
		$16V \leq V_i \leq 22V, T_J=25^\circ C$			120	mV
Quiescent Current	I_q	$T_J=25^\circ C$		4.4	8.0	mA
Quiescent Current Change	ΔI_q	$5.0mA \leq I_o \leq 1.0A$			0.5	mA
		$15V \leq V_i \leq 30V$			0.8	mA
Output Voltage Drift	$\Delta V_o/\Delta T$	$I_o=5mA$		1.5		mV/°C
Output Noise Voltage	V_N	$f=10Hz$ to $100KHz, T_J=25^\circ C$		42		μV/Vo
Ripple Rejection	RR	$f=120Hz, 15V \leq V_i \leq 25V$		60		dB
Dropout Voltage	V_d	$I_o=1.0A, T_J=25^\circ C$		2.0		V
Output Resistance	R_o	$f = 1KHz$		18		mΩ
Short Circuit Current	I_{sc}	$T_J=25^\circ C$		200		mA

* Pulse test.

TYPICAL APPLICATION



Note: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.

