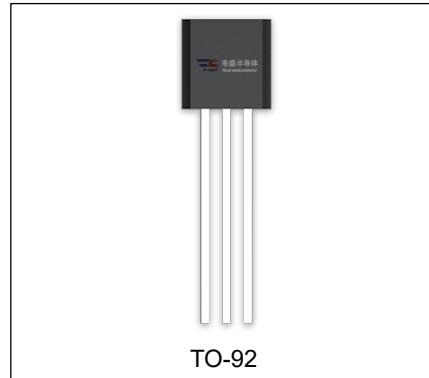


VS78L05 Three-terminal positive voltage regulator

FEATURES

- Maximum output current
 I_{OM} : 0.1A
- Output voltage
 V_O : 5V
- Continuous total dissipation
 P_D : 0.625 W ($T_a = 25^\circ\text{C}$)



ORDERING INFORMATION

Part Number	Package	Packing Method	Pack Quantity
VS78L05	TO-92	Bulk	1000pcs/Bag
VS78L05-TA	TO-92	Tape	2000pcs/Box

ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

Parameter	Symbol	Value	Unit
Input Voltage	V_i	30	V
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	160	°C/W
Operating Junction Temperature Range	T_{OPR}	-40~+125	°C
Storage Temperature Range	T_{STG}	-65~+150	°C

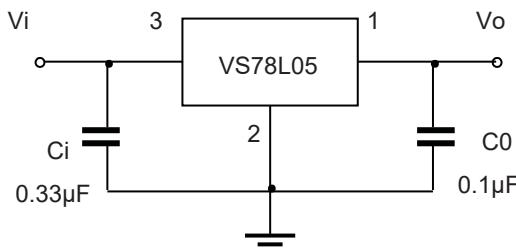
T_a=25 °C unless otherwise specified

(Vi=10V,Io=40mA,Ci=0.33μF,,Co=0.1μF, unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Output voltage	Vo	T _J =25°C	4.85	5.0	5.15	V
			4.90	5.0	5.10	V
		7V≤Vi≤20V, Io=1mA~40mA	4.75	5.0	5.25	V
		Io=1mA~70mA	4.75	5.0	5.25	V
Load Regulation	ΔVo	Io=1mA~100mA,T _J =25°C		15	60	mV
		Io=1mA~40mA,T _J =25°C		8	30	mV
Line regulation	ΔVo	7V≤Vi≤20V		32	150	mV
		8V≤Vi≤20V,T _J =25°C		26	100	mV
Quiescent Current	Iq	T _J =25°C		3.8	6	mA
Quiescent Current Change	ΔIq	8V≤Vi≤20V			1.5	mA
	ΔIq	1mA≤Vi≤40mA			0.1	mA
Output Noise Voltage	V _N	10Hz≤f≤100KHz,T _J =25°C		42		μV/Vo
Ripple Rejection	RR	8V≤Vi≤20V,f=120Hz	41	49		dB
Dropout Voltage	Vd	T _J =25°C		1.7		V

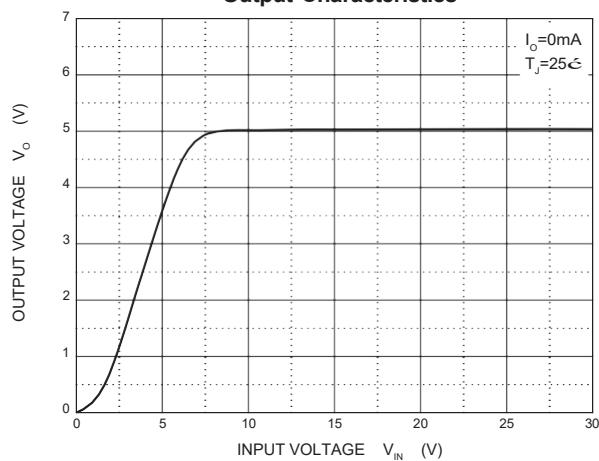
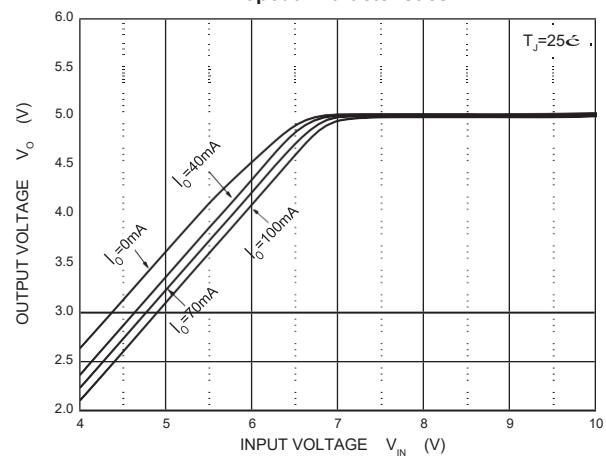
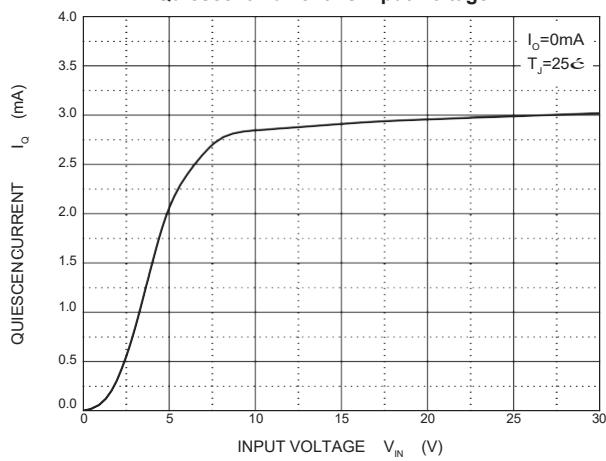
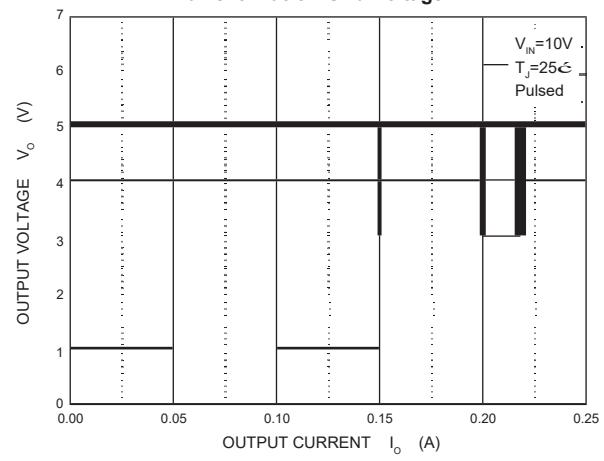
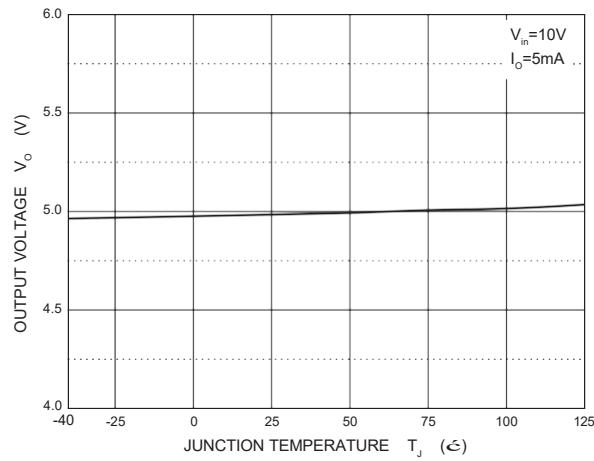
* Pulse test.

TYPICAL APPLICATION



Note: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as

Possible to the regulators.

Output Characteristics

Dropout Characteristics

Quiescent Current vs Input Voltage

Current Cut-off Grid Voltage

Output Voltage vs Junction Temperature

Power Derating Curve
