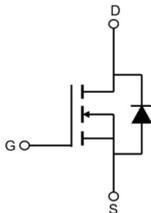


Description

Features <ul style="list-style-type: none"> ● 30V, 190A ● $R_{DS(ON)} < 2.6m\Omega @ V_{GS} = 10V$ ● $R_{DS(ON)} < 4.2m\Omega @ V_{GS} = 4.5V$ ● Advanced Trench Technology ● Excellent $R_{DS(ON)}$ and Low Gate Charge ● Lead free product is acquired 	Application <ul style="list-style-type: none"> ● Load Switch ● PWM Application ● Power management <p style="text-align: center;">100% UIS 100% ΔVds</p>
 TO-252	 Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
VSM190N03-T2	VSM190N03	TAPING	TO-252	13inch	2500	25000

Absolute Maximum Ratings (T_C=25°C unless otherwise specified)

Symbol	Parameter	Max.	Units
V _{DSS}	Drain-Source Voltage	30	V
V _{GSS}	Gate-Source Voltage	±20	V
I _D	Continuous Drain Current	T _C = 25°C	190
		T _C = 100°C	124
I _{DM}	Pulsed Drain Current ^{note1}	760	A
E _{AS}	Single Pulsed Avalanche Energy ^{note2}	361	mJ
P _D	Power Dissipation	T _C = 25°C	178
R _{θJC}	Thermal Resistance, Junction to Case	0.84	°C/W
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +175	°C

Electrical Characteristics ($T_C=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=30V, V_{GS}=0V,$	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS}=0V, V_{GS}=\pm 20V$	-	-	± 100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.6	2.5	V
$R_{DS(on)}$	Static Drain-Source on-Resistance <small>note3</small>	$V_{GS}=10V, I_D=20A$	-	2	2.6	m Ω
		$V_{GS}=4.5V, I_D=10A$	-	3	4.2	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=15V, V_{GS}=0V,$ $f=1.0MHz$	-	6847	-	pF
C_{oss}	Output Capacitance		-	940	-	pF
C_{rss}	Reverse Transfer Capacitance		-	604	-	pF
Q_g	Total Gate Charge	$V_{DS}=15V, I_D=30A,$ $V_{GS}=10V$	-	93	-	nC
Q_{gs}	Gate-Source Charge		-	14	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	21	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=15V,$ $I_D=30A, R_{GEN}=3\Omega,$ $V_{GS}=10V$	-	16	-	ns
t_r	Turn-on Rise Time		-	9	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	65	-	ns
t_f	Turn-off Fall Time		-	18	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	190	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	760	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0V, I_S=30A$	-	-	1.2	V
t_{rr}	Body Diode Reverse Recovery Time	$I_F=20A, di/dt=100A/\mu s$	-	29	-	ns
Q_{rr}	Body Diode Reverse Recovery Charge		-	19	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

 2. EAS condition: $T_J=25^{\circ}\text{C}, V_{DD}=15V, V_G=10V, L=0.5mH, R_G=25\Omega, I_{AS}=38A$

 3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$

Typical Performance Characteristics

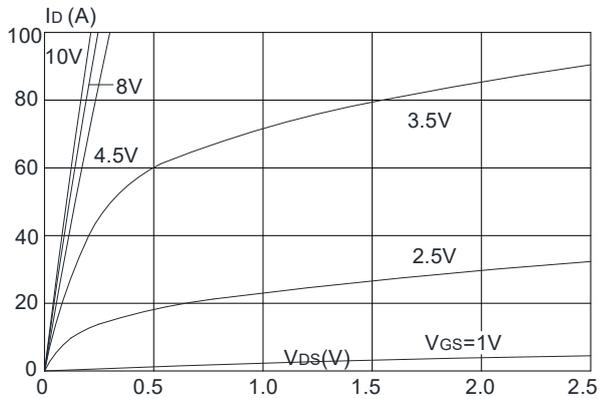
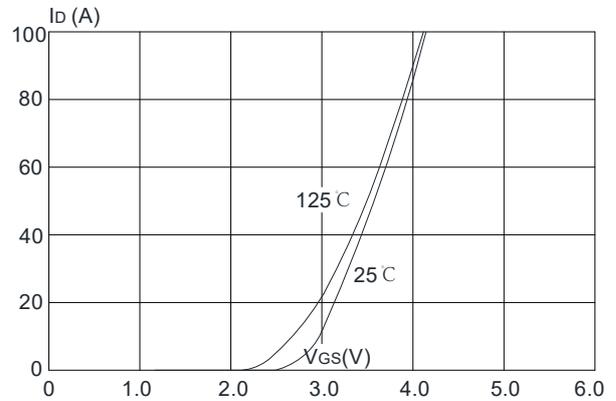
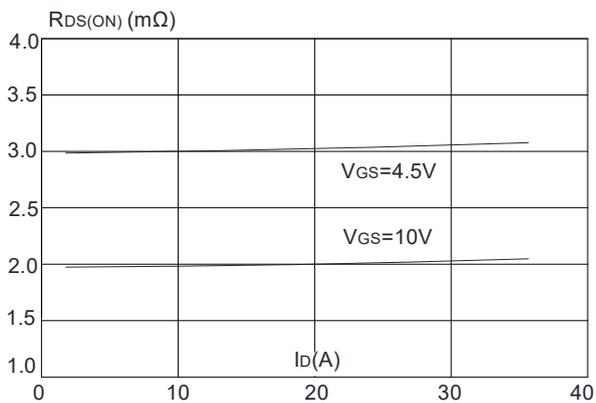
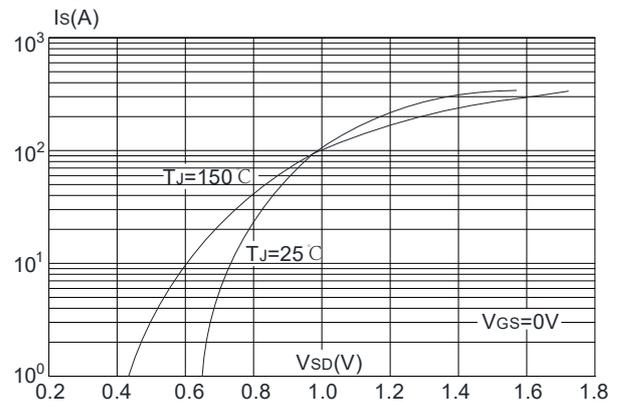
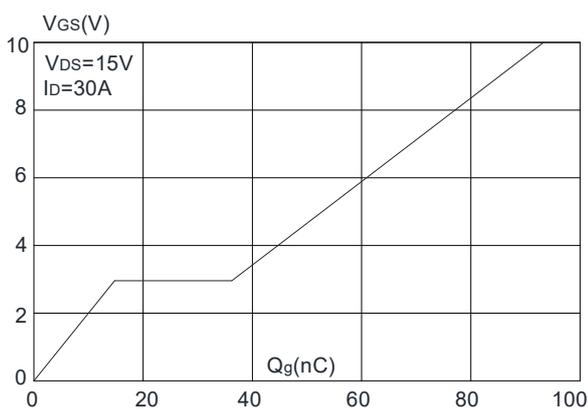
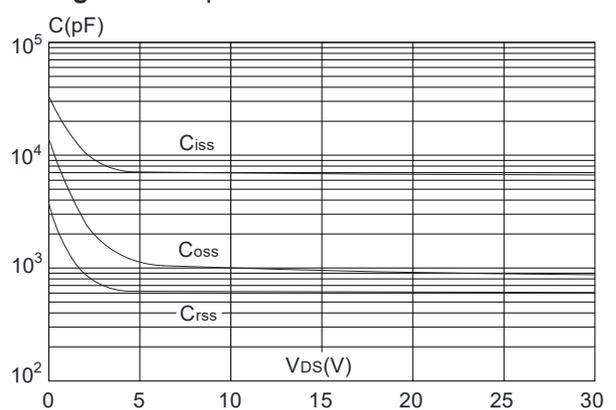
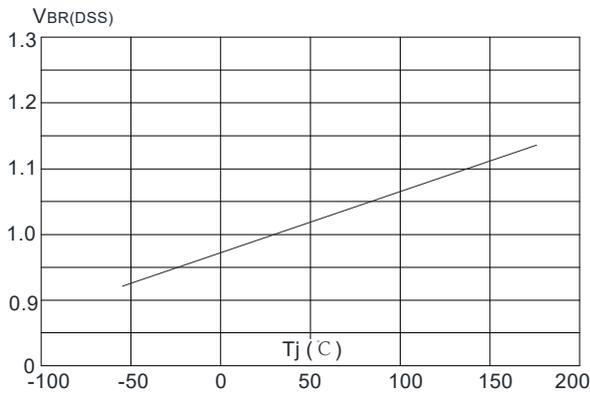
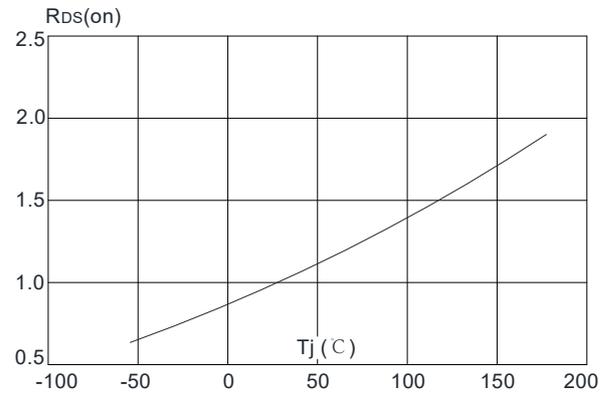
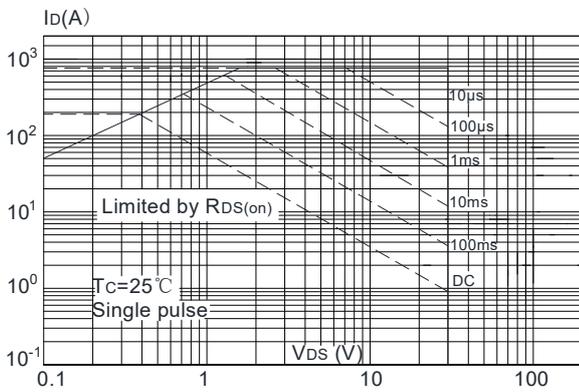
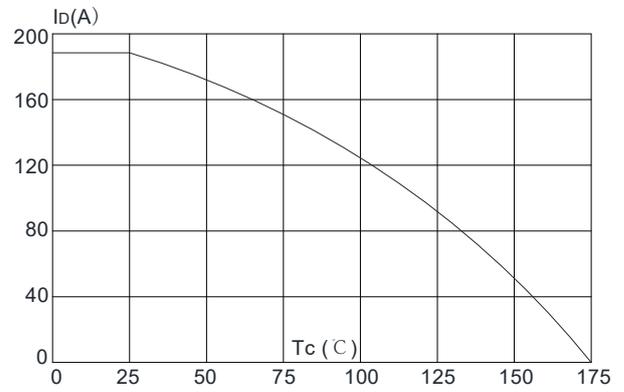
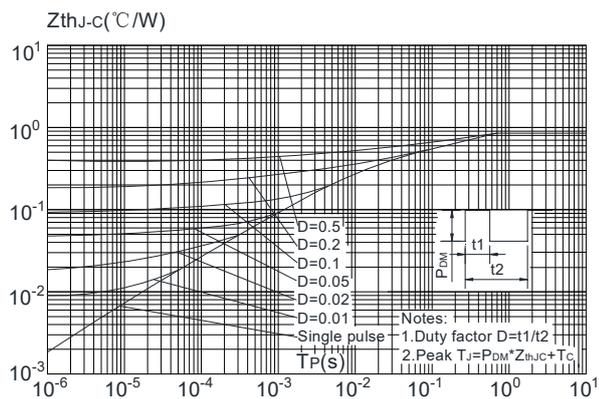
Figure 1: Output Characteristics

Figure 2: Typical Transfer Characteristics

Figure 3: On-resistance vs. Drain Current

Figure 4: Body Diode Characteristics

Figure 5: Gate Charge Characteristics

Figure 6: Capacitance Characteristics


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

Figure 8: Normalized on Resistance vs. Junction Temperature

Figure 9: Maximum Safe Operating Area

Figure 10: Maximum Continuous Drain Current vs. Case Temperature

Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case


Test Circuit

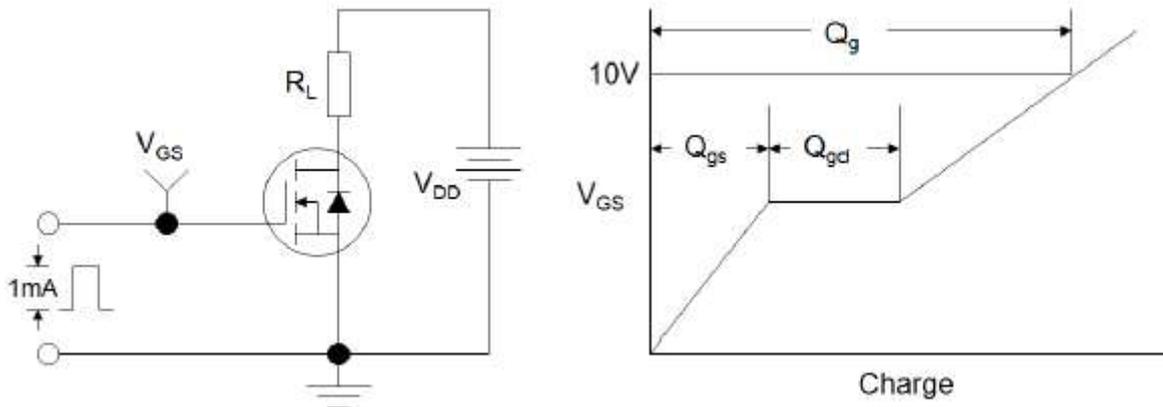


Figure1:Gate Charge Test Circuit & Waveform

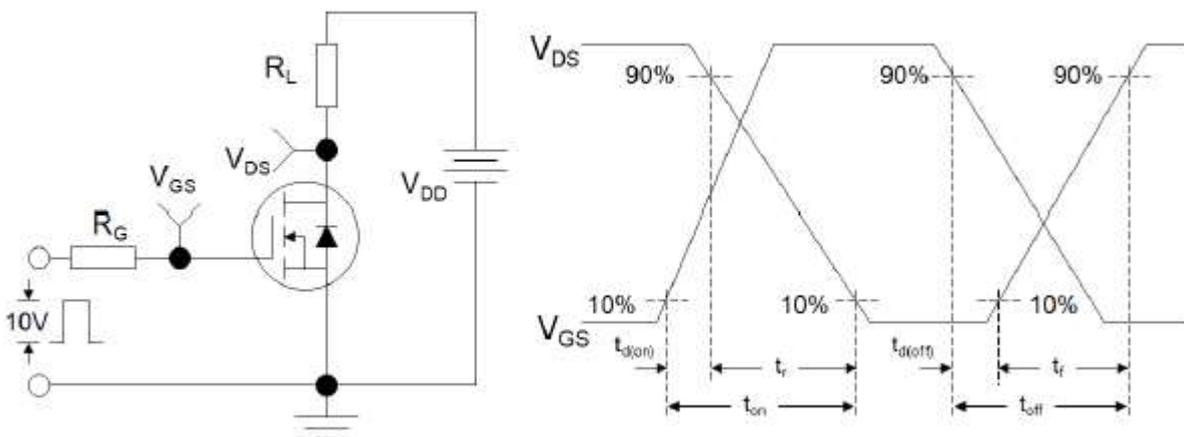


Figure 2: Resistive Switching Test Circuit & Waveforms

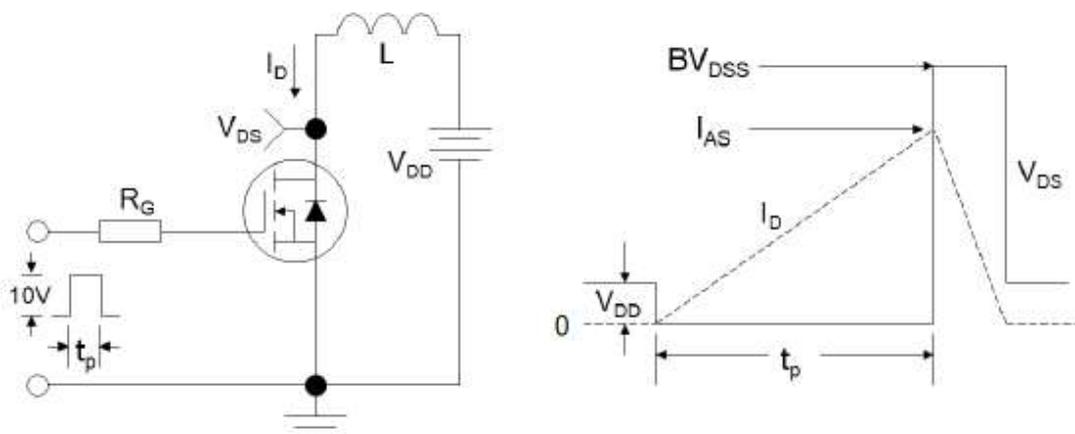


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms