

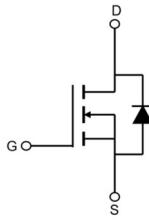
## Description

<b>Features</b>	<b>Application</b>
<ul style="list-style-type: none"> <li>● 30V, 190A</li> <li>● <math>R_{DS(ON)} &lt; 2.4\text{m}\Omega</math> @ <math>V_{GS} = 10\text{V}</math></li> <li>● <math>R_{DS(ON)} &lt; 4.2\text{m}\Omega</math> @ <math>V_{GS} = 4.5\text{V}</math></li> <li>● Advanced Trench Technology</li> <li>● Excellent <math>R_{DS(ON)}</math> and Low Gate Charge</li> <li>● Lead free product is acquired</li> </ul>	<ul style="list-style-type: none"> <li>● Load Switch</li> <li>● PWM Application</li> <li>● Power management</li> </ul>



TO-263



Schematic Diagram

## Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
VSM190N03-T3	VSM190N03	TAPING	TO-263	13inch	800	4000

## Absolute Maximum Ratings ( $T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter		Max.	Units
$V_{DSS}$	Drain-Source Voltage		30	V
$V_{GSS}$	Gate-Source Voltage		$\pm 20$	V
$I_D$	Continuous Drain Current	$T_C = 25^\circ\text{C}$	190	A
		$T_C = 100^\circ\text{C}$	124	A
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>		760	A
$E_{AS}$	Single Pulsed Avalanche Energy <sup>note2</sup>		441	mJ
$P_D$	Power Dissipation	$T_C = 25^\circ\text{C}$	173	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		0.87	°C/W
$T_J, T_{STG}$	Operating and Storage Temperature Range		-55 to +175	°C

## Electrical Characteristics ( $T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	30	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS}=30\text{V}$ , $V_{GS}=0\text{V}$ ,	-	-	1.0	$\mu\text{A}$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS}=0\text{V}$ , $V_{GS}=\pm 20\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D=250\mu\text{A}$	1.0	1.6	2.5	V
$R_{DS(\text{on})}$ note3	Static Drain-Source on-Resistance	$V_{GS}=10\text{V}$ , $I_D=30\text{A}$	-	1.9	2.4	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=20\text{A}$	-	3	4.2	
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=15\text{V}$ , $V_{GS}=0\text{V}$ , $f=1.0\text{MHz}$	-	6847	-	pF
$C_{oss}$	Output Capacitance		-	940	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	604	-	pF
$Q_g$	Total Gate Charge	$V_{DS}=15\text{V}$ , $I_D=30\text{A}$ , $V_{GS}=10\text{V}$	-	93	-	nC
$Q_{gs}$	Gate-Source Charge		-	14	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	21	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=15\text{V}$ , $I_D=30\text{A}$ , $R_{\text{GEN}}=3\Omega$ , $V_{GS}=10\text{V}$	-	16	-	ns
$t_r$	Turn-on Rise Time		-	9	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	65	-	ns
$t_f$	Turn-off Fall Time		-	18	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_s$	Maximum Continuous Drain to Source Diode Forward Current	-	-	190	-	A
$I_{sM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	760	-	A
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s=30\text{A}$	-	-	1.2	V
$trr$	Body Diode Reverse Recovery Time	$I_F=20\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$	-	29	-	ns
$Qrr$	Body Diode Reverse Recovery Charge		-	19	-	nC

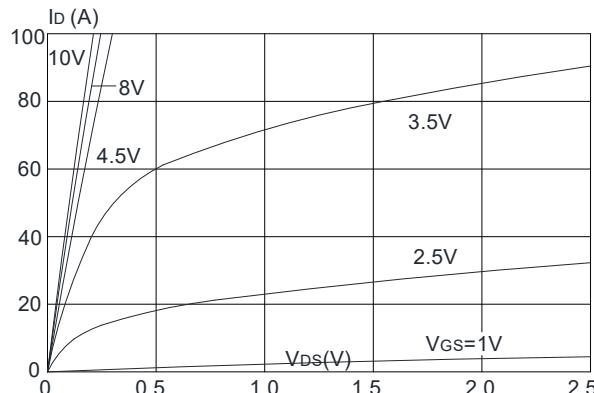
Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition:  $T_J=25^\circ\text{C}$ ,  $V_{DD}=15\text{V}$ ,  $V_G=10\text{V}$ ,  $L=0.5\text{mH}$ ,  $R_G=25\Omega$ ,  $I_{AS}=42\text{A}$

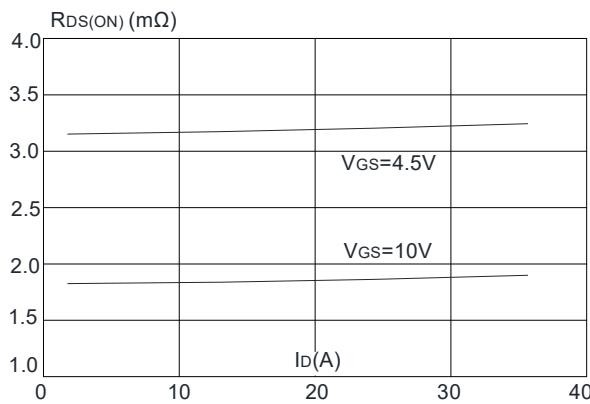
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$ , Duty Cycle $\leq 0.5\%$

## Typical Performance Characteristics

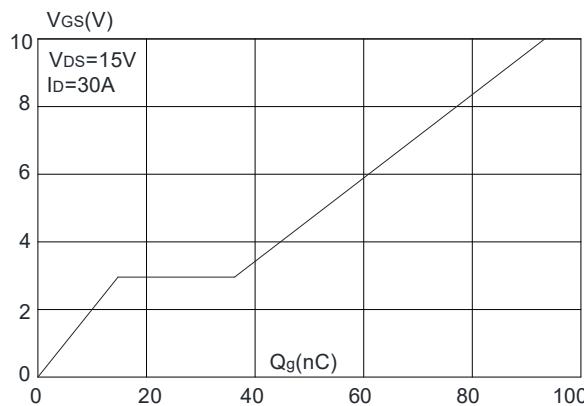
**Figure1:** Output Characteristics



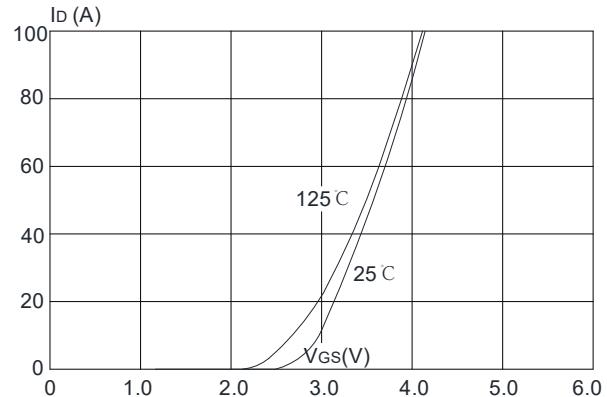
**Figure 3:** On-resistance vs. Drain Current



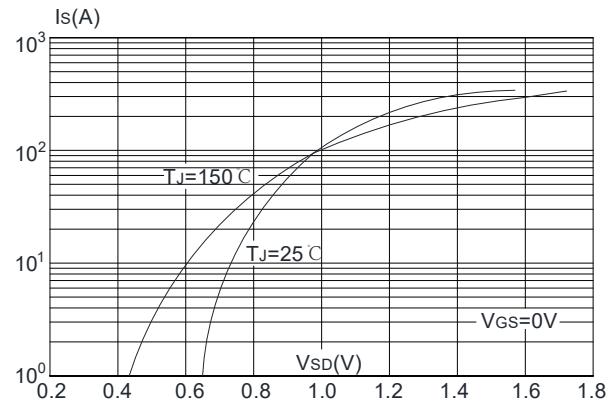
**Figure 5:** Gate Charge Characteristics



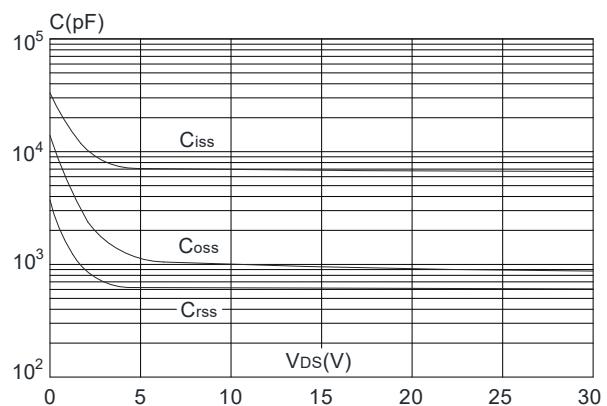
**Figure 2:** Typical Transfer Characteristics



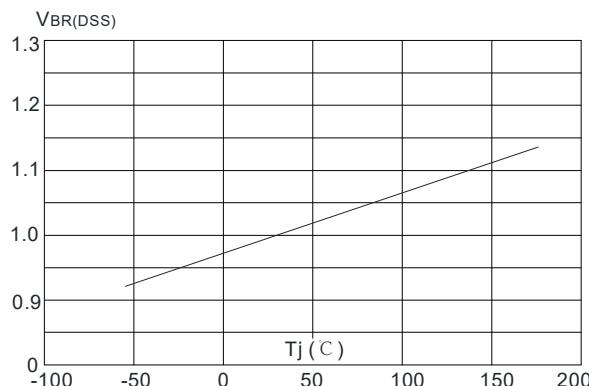
**Figure 4:** Body Diode Characteristics



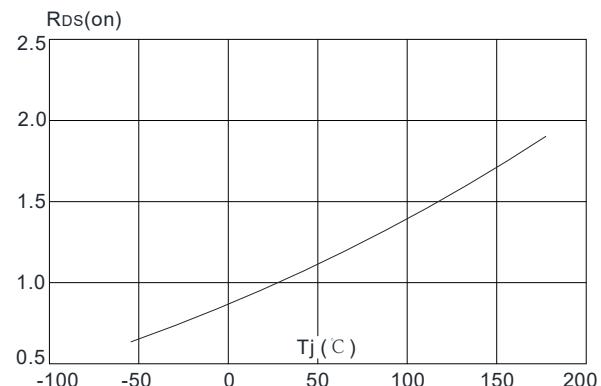
**Figure 6:** Capacitance Characteristics



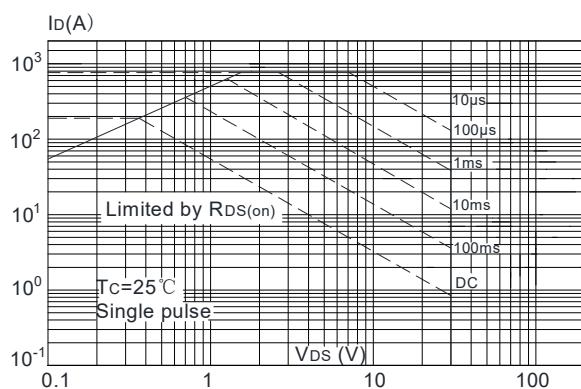
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



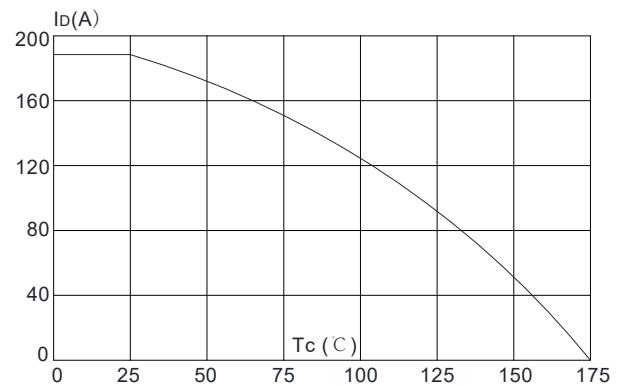
**Figure 8:** Normalized on Resistance vs. Junction Temperature



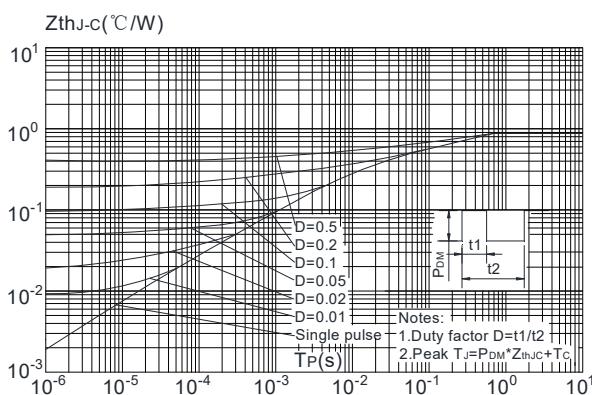
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature



**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case



## Test Circuit

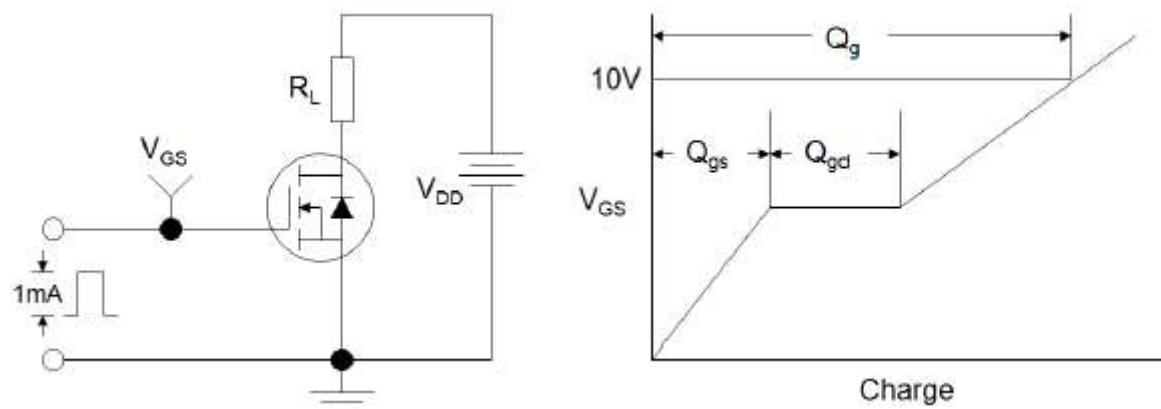


Figure1:Gate Charge Test Circuit & Waveform

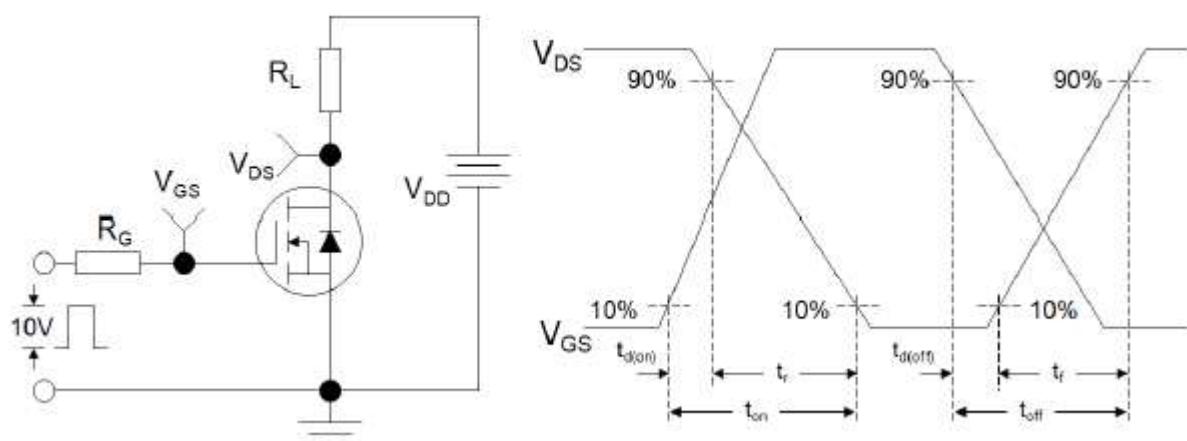


Figure 2: Resistive Switching Test Circuit & Waveforms

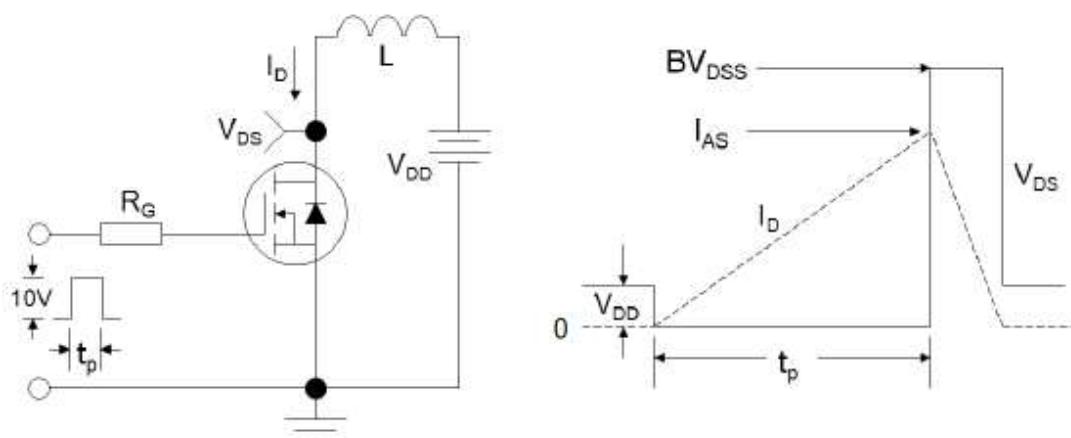


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms