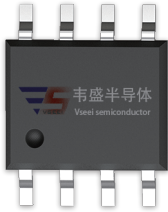
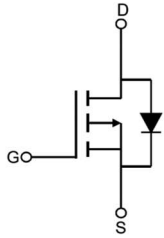


Description

Features <ul style="list-style-type: none"> ● $V_{DS} = -30V$, $I_D = -5.1A$ $R_{DS(ON)} < 55m\Omega @ V_{GS} = -10V$ $R_{DS(ON)} < 90m\Omega @ V_{GS} = -4.5V$ ● Advanced Trench Technology ● Excellent $R_{DS(ON)}$ and Low Gate Charge ● Lead free product is acquired 	Application <ul style="list-style-type: none"> ● PWM Applications ● Load Switch ● Power Management
 <p>SOP-8</p>	 <p>Schematic Diagram</p>

Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
VSM5P03-S8	VSM5P03	TAPING	SOP-8	13inch	4000	48000

Absolute Maximum Ratings ($T_A = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Max.	Units	
V_{DSS}	Drain-Source Voltage	-30	V	
V_{GSS}	Gate-Source Voltage	± 20	V	
I_D	Continuous Drain Current	$T_A = 25^\circ C$	-5.1	A
		$T_A = 100^\circ C$	-3.3	A
I_{DM}	Pulsed Drain Current ^{note1}	-20.4	A	
P_D	Power Dissipation	$T_A = 25^\circ C$	2.15	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	58	$^\circ C/W$	
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150	$^\circ C$	

Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=-30V, V_{GS}=0V,$	-	-	-1	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS}=0V, V_{GS}=\pm 20V$	-	-	± 100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1.0	-1.6	-2.5	V
$R_{DS(on)}$	Static Drain-Source on-Resistance <small>note2</small>	$V_{GS}=-10V, I_D=-5A$	-	43	55	m Ω
		$V_{GS}=-4.5V, I_D=-4A$	-	65	90	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=-15V, V_{GS}=0V,$ $f=1.0MHz$	-	596	-	pF
C_{oss}	Output Capacitance		-	95	-	pF
C_{rss}	Reverse Transfer Capacitance		-	68	-	pF
Q_g	Total Gate Charge	$V_{DS}=-15V, I_D=-5.1A,$ $V_{GS}=-10V$	-	6.8	-	nC
Q_{gs}	Gate-Source Charge		-	1	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	1.4	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=-15V, I_D=-1A,$ $V_{GS}=-10V, R_{GEN}=2.5\Omega$	-	14	-	ns
t_r	Turn-on Rise Time		-	61	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	19	-	ns
t_f	Turn-off Fall Time		-	10	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	-5.1	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-20.4	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0V, I_S=-5.1A$	-	-0.8	-1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

 2. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

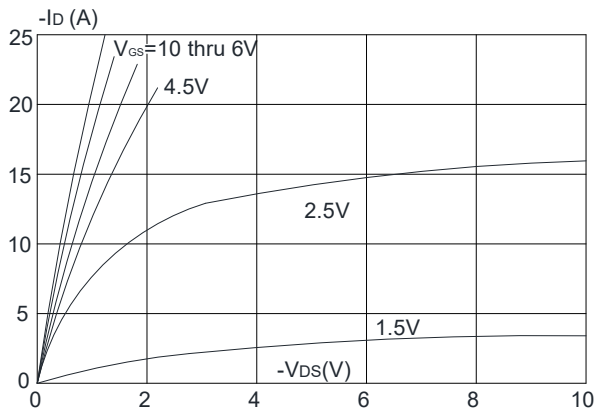
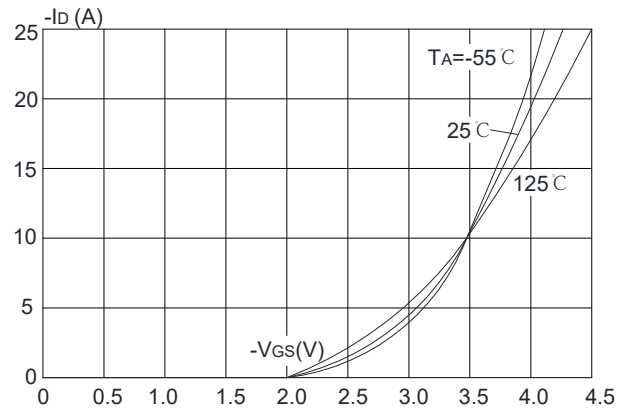
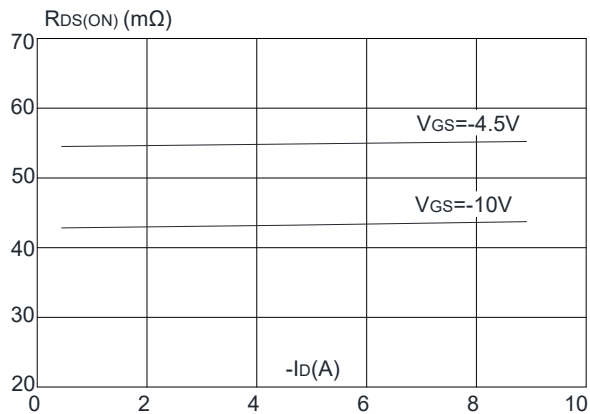
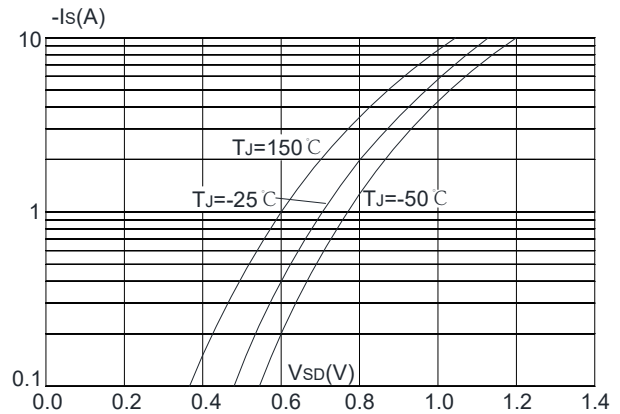
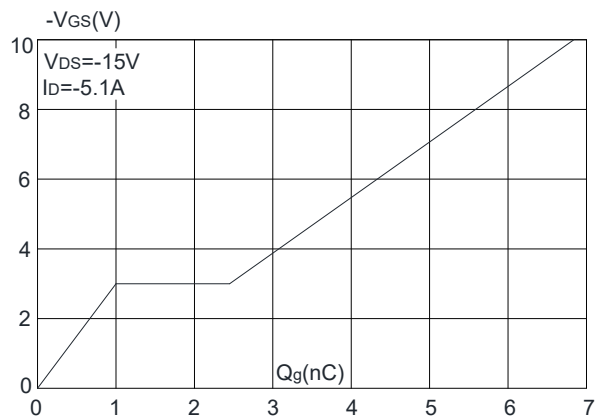
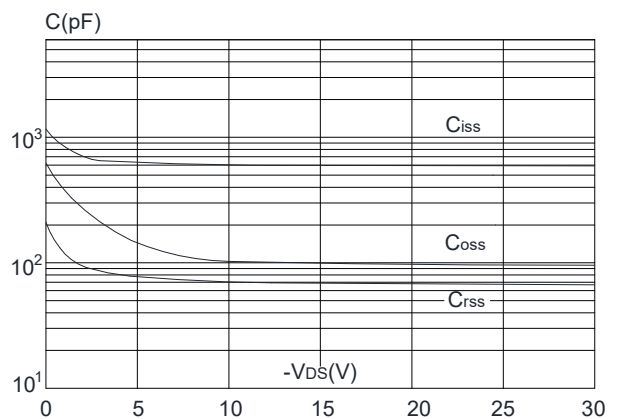
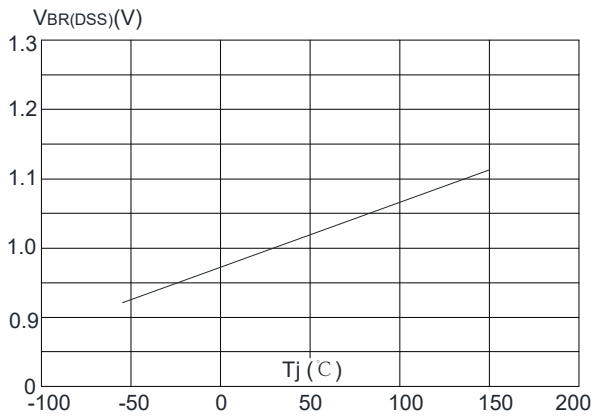
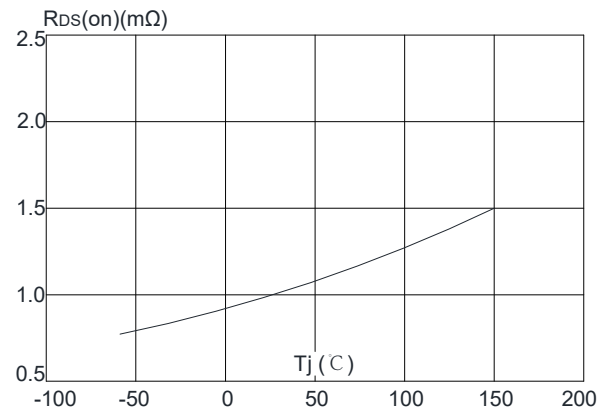
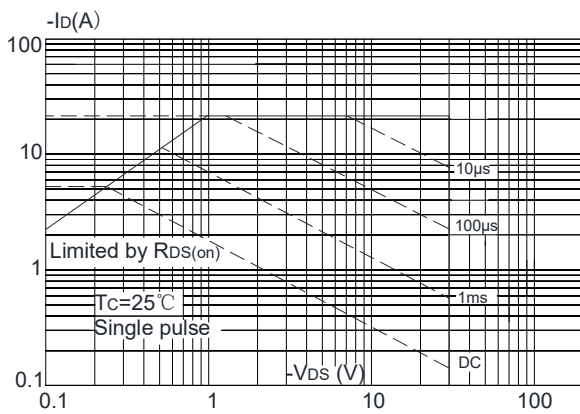
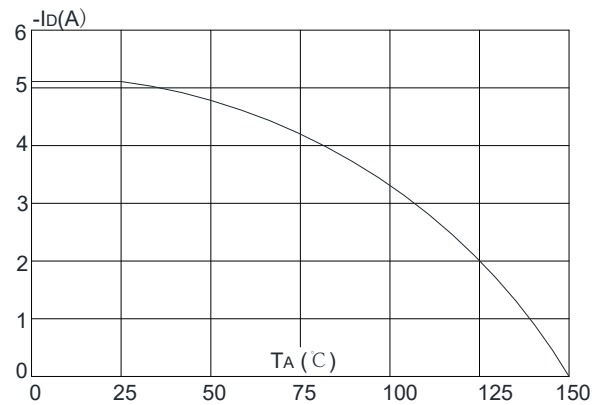
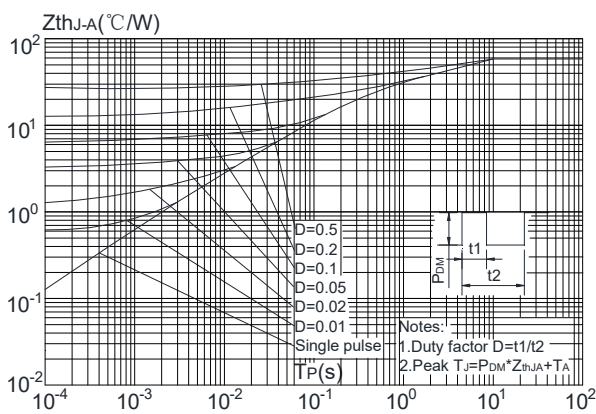
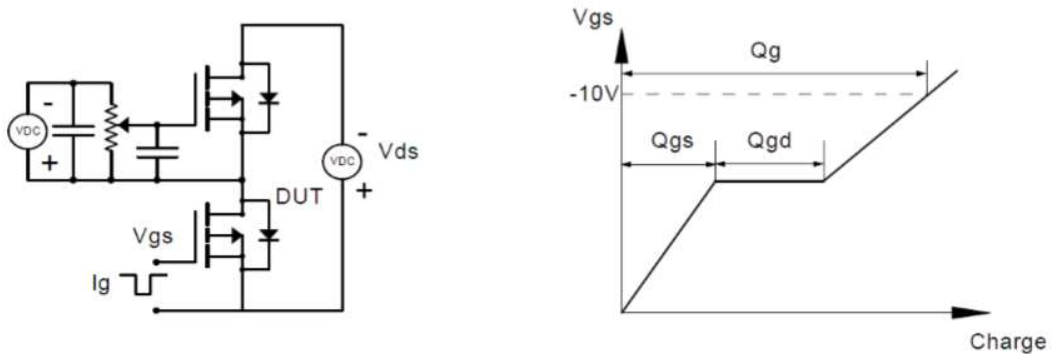
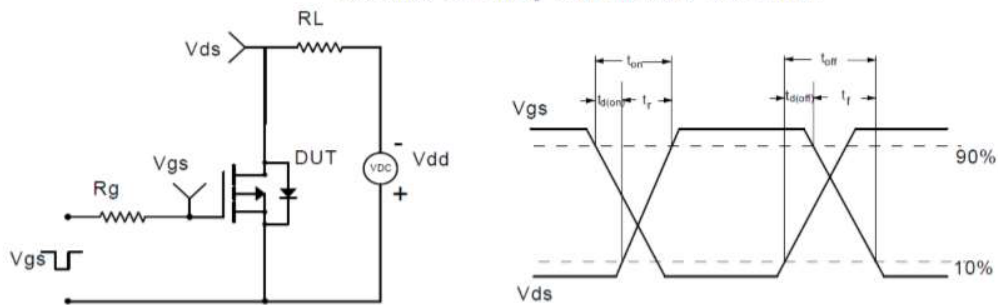
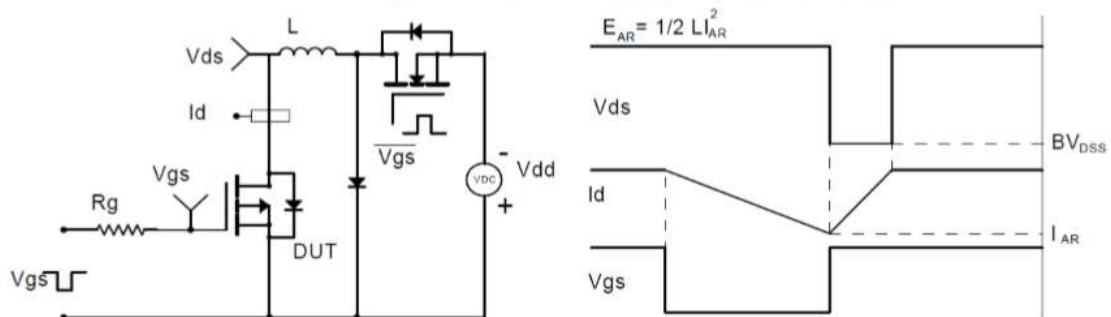
Figure 1: Output Characteristics

Figure 2: Typical Transfer Characteristics

Figure 3: On-resistance vs. Drain Current

Figure 4: Body Diode Characteristics

Figure 5: Gate Charge Characteristics

Figure 6: Capacitance Characteristics


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

Figure 8: Normalized on Resistance vs. Junction Temperature

Figure 9: Maximum Safe Operating Area

Figure 10: Maximum Continuous Drain Current vs. Case Temperature

Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient


Typical Performance Characteristics

Gate Charge Test Circuit & Waveform

Resistive Switching Test Circuit & Waveforms

Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

Diode Recovery Test Circuit & Waveforms
