Description

The VST08N014 uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g. This device is ideal for high-frequency switching and synchronous rectification.

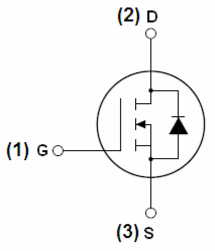
General Features

- V_{DS} =85V,I_D =350A
  $R_{DS(ON)}$ <1.85mΩ @ V_{GS}=10V
- Excellent gate charge x $R_{DS(on)}$ product
- Very low on-resistance $R_{DS(on)}$
- 175 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

TO-247



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
VST08N014-T7	VST08N014	TO-247	-	-	-

Absolute Maximum Ratings (T_C=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	85	V
Gate-Source Voltage	V_{GS}	±20	V
Drain Current-Continuous	I_D	350	A
Drain Current-Continuous(T_C=100℃)	I_D (100℃)	280	A
Pulsed Drain Current	I_{DM}	1400	A
Maximum Power Dissipation	P_D	520	W
Derating factor		3.47	W/℃
Single pulse avalanche energy (Note 5)	E_{AS}	3800	mJ
Operating Junction and Storage Temperature Range	T_J,T_{STG}	-55 To 175	℃

Thermal Characteristic

Thermal Resistance,Junction-to-Case(Note 2)	$R_{θJC}$	0.29	℃/W

Electrical Characteristics (T_C=25℃unless otherwise noted)

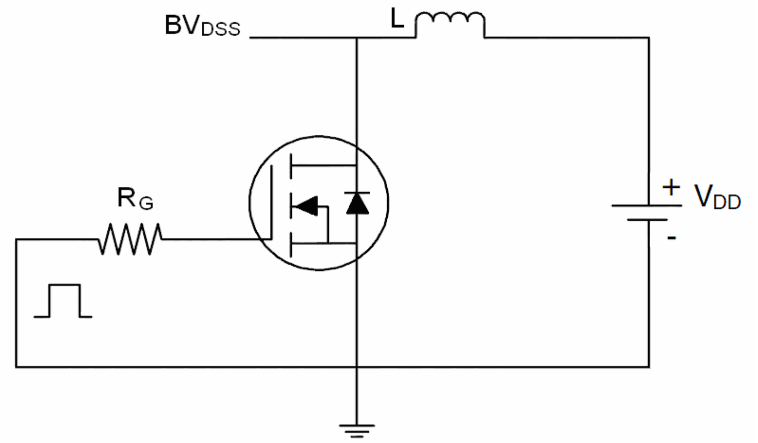
Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V I_D=250μA	85	90	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=85V,V_{GS}=0V	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	V_{GS}=±20V,V_{DS}=0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS},I_D=250μA	3	3.8	5	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D=175A	-	1.4	1.85	mΩ
Forward Transconductance	g_{FS}	V_{DS}=10V,I_D=175A	-	150	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C_{lss}	V_{DS}=40V,V_{GS}=0V, F=1.0MHz	-	19500	-	PF
Output Capacitance	C_{oss}		-	2990	-	PF
Reverse Transfer Capacitance	C_{rss}		-	200	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	V_{DD}=40V,I_D=100A V_{GS}=10V,R_G=1.8Ω	-	35	-	nS
Turn-on Rise Time	t_r		-	98	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	110	-	nS
Turn-Off Fall Time	t_f		-	45	-	nS
Total Gate Charge	Q_g	V_{DS}=40V,I_D=100A, V_{GS}=10V	-	324		nC
Gate-Source Charge	Q_{gs}		-	123		nC
Gate-Drain Charge	Q_{gd}		-	88		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	V_{GS}=0V,I_F= 175A	-		1.2	V
Diode Forward Current (Note 2)	I_S		-	-	350	A
Reverse Recovery Time	t_{rr}	T_J = 25°C, I_F = I_S di/dt = 100A/μs(Note3)	-	155		nS
Reverse Recovery Charge	Qrr		-	436		nC

Notes:

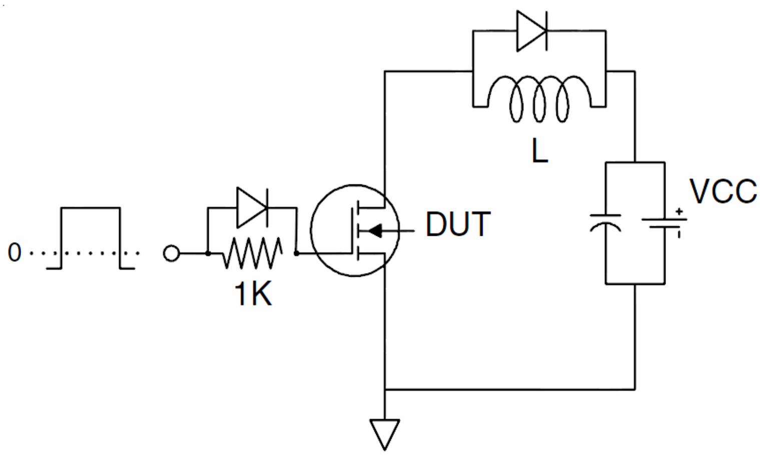
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production
5. EAS condition : Tj=25℃,V_{DD}=42.5V,V_G=10V,L=1mH,Rg=25Ω

Test Circuit

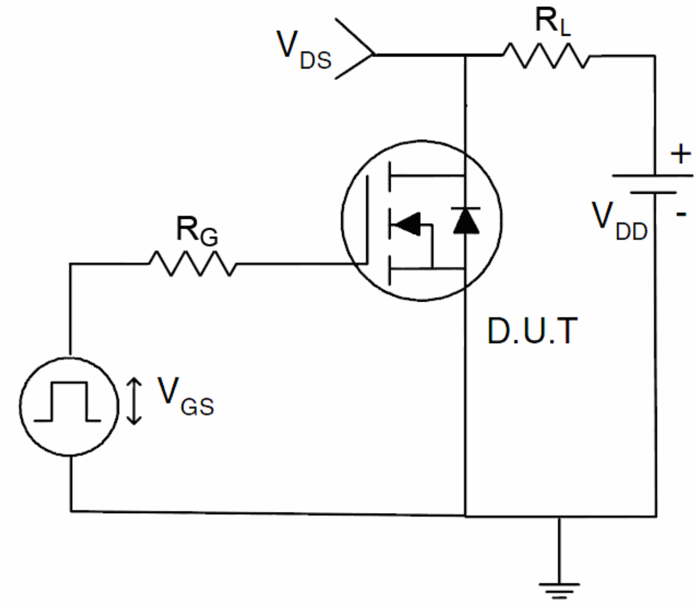
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit



Typical Electrical and Thermal Characteristics

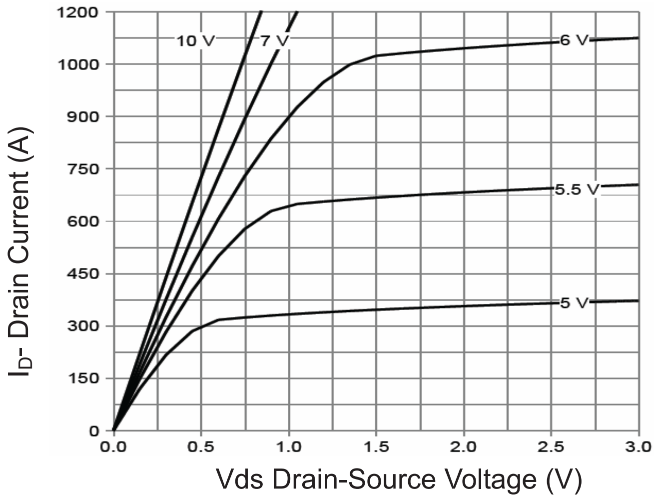


Figure 1 Output Characteristics

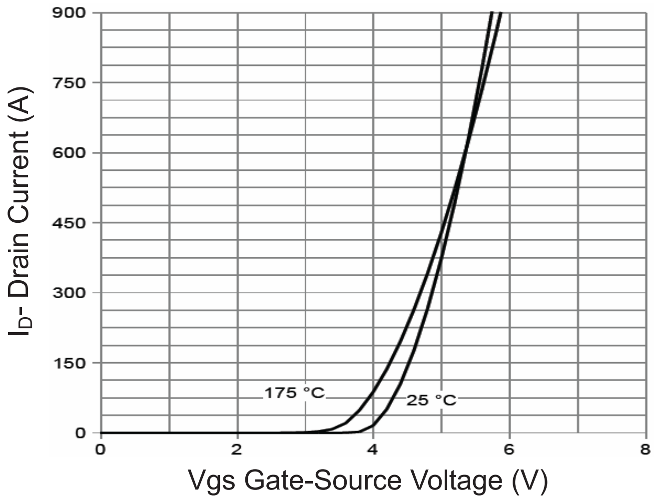


Figure 2 Transfer Characteristics

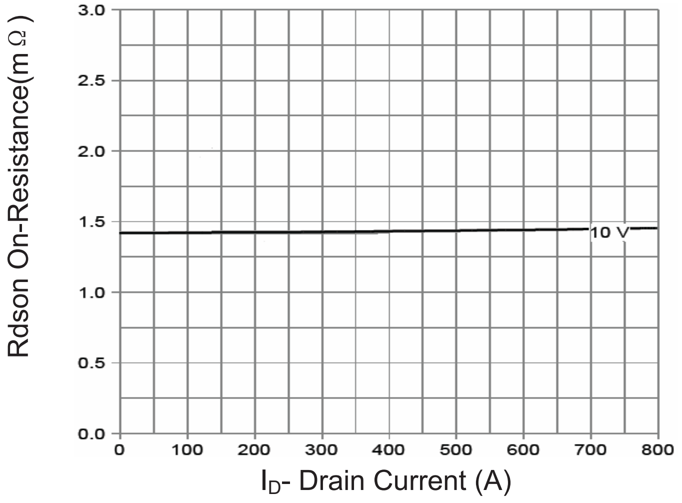


Figure 3 Rdson- Drain Current

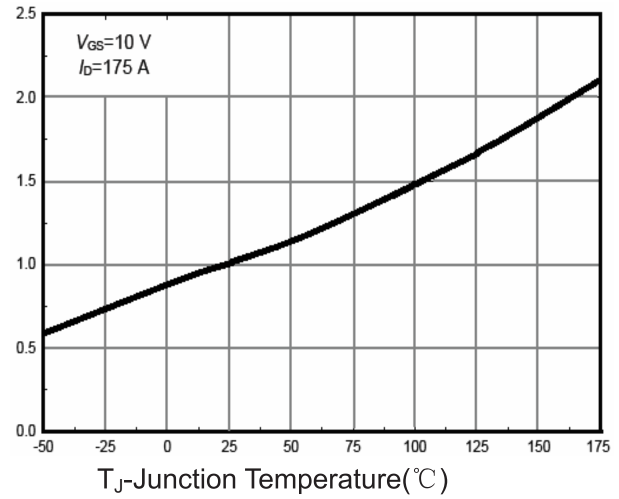


Figure 4 Rdson-JunctionTemperature

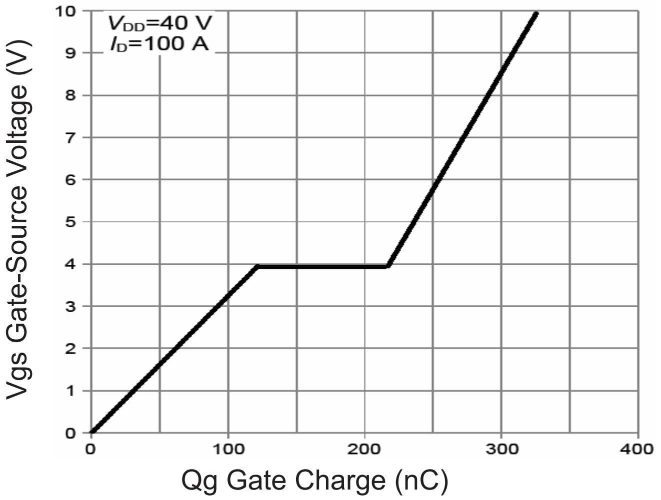


Figure 5 Gate Charge

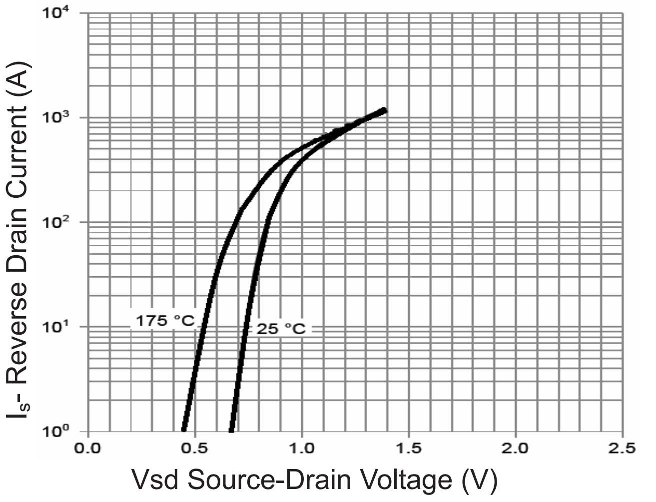


Figure 6 Source- Drain Diode Forward

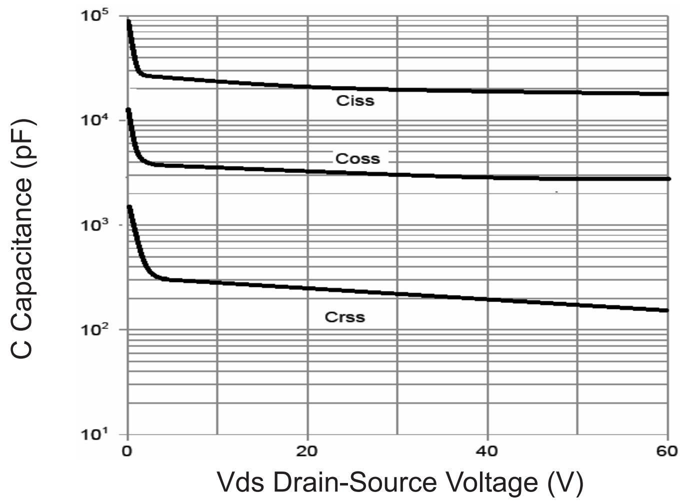


Figure 7 Capacitance vs Vds

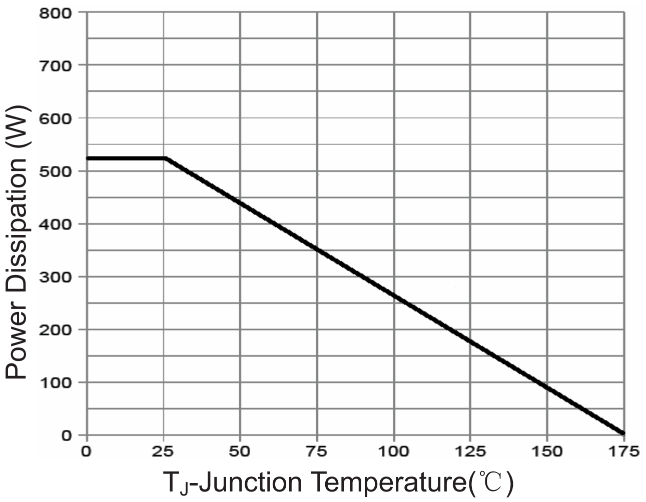


Figure 9 Power De-rating

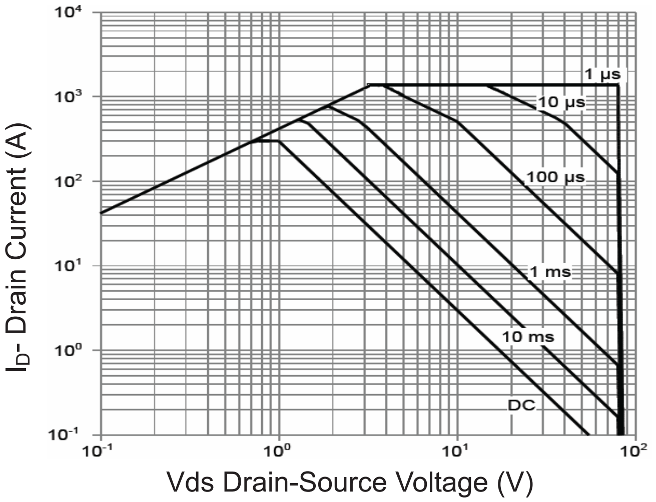


Figure 8 Safe Operation Area

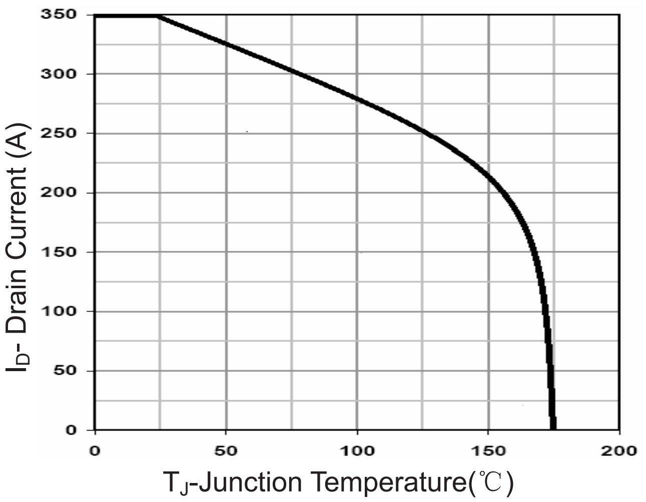


Figure 10 Current De-rating

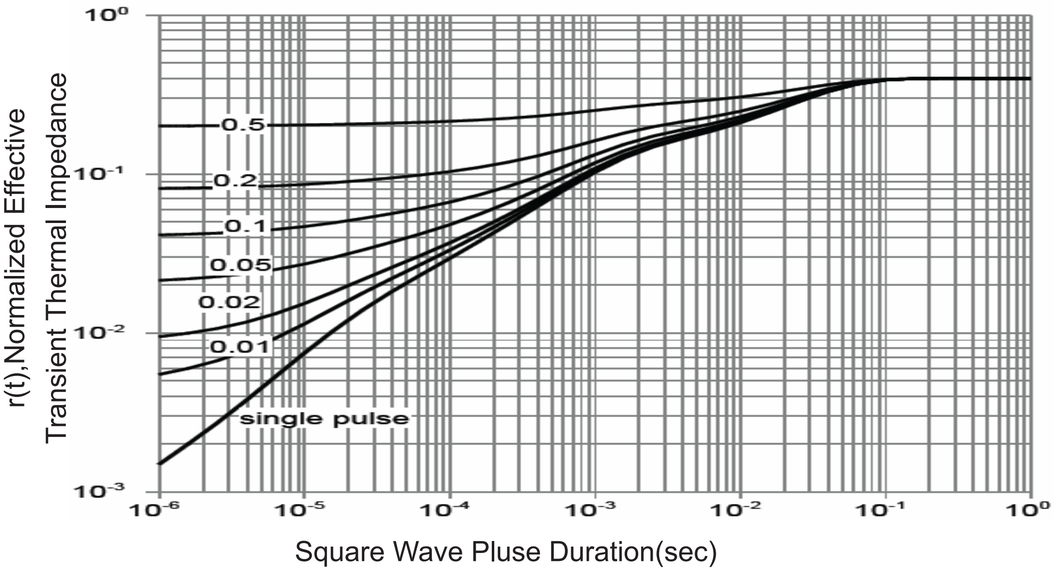


Figure 11 Normalized Maximum Transient Thermal Impedance