
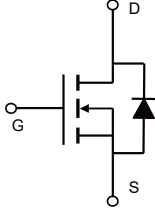


<p><b>Description</b></p> <p>These N-Channel enhancement mode power field effect transistors are using split gate trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and with stand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.</p> <p><b>Features</b></p> <ul style="list-style-type: none"> <li>◆ 100V, 124A, <math>R_{DS(on),max} = 4.5m\Omega @ V_{GS} = 10V</math></li> <li>◆ Improved dv/dt capability</li> <li>◆ Fast switching</li> <li>◆ 100% EAS Guaranteed</li> <li>◆ Green device available</li> </ul> <p><b>Applications</b></p> <ul style="list-style-type: none"> <li>◆ MOTOR Driver</li> <li>◆ BMS</li> <li>◆ High frequency switching and synchronous rectification</li> </ul>	<p><b>Product Summary</b></p> <table> <tr> <td><math>V_{DSS}</math></td> <td>100V</td> </tr> <tr> <td><math>R_{DS(on),max} @ V_{GS}=10V</math></td> <td>4.5m<math>\Omega</math></td> </tr> <tr> <td><math>I_D</math></td> <td>124A</td> </tr> </table> <p><b>Pin Configuration</b></p> <div style="display: flex; justify-content: space-around; align-items: center;"> <div style="text-align: center;">  <p>TO-263</p> </div> <div style="text-align: center;">  <p>Schematic</p> </div> </div>	$V_{DSS}$	100V	$R_{DS(on),max} @ V_{GS}=10V$	4.5m $\Omega$	$I_D$	124A
$V_{DSS}$	100V						
$R_{DS(on),max} @ V_{GS}=10V$	4.5m $\Omega$						
$I_D$	124A						

**Absolute Maximum Ratings**  $T_C = 25^\circ C$  unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	100	V
Continuous drain current ( $T_C = 25^\circ C$ ) ( $T_C = 100^\circ C$ )	$I_D$	124	A
		82	A
Pulsed drain current <sup>1)</sup>	$I_{DM}$	372	A
Gate-Source voltage	$V_{GSS}$	$\pm 20$	V
Avalanche energy <sup>2)</sup>	$E_{AS}$	7.2	mJ
Power Dissipation	$P_D$	137	W
Storage Temperature Range	$T_{STG}$	-55 to +150	$^\circ C$
Operating Junction Temperature Range	$T_J$	-55 to +150	$^\circ C$

**Thermal Characteristics**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.91	$^\circ C/W$
Thermal Resistance Junction-to-Ambient	$R_{\theta JA}$	62	$^\circ C/W$

**Package Marking and Ordering Information**

Device	Device Package	Marking	Units/Reel
VST10N045-T3	TO-263	VST10N045-T3	800

**Electrical Characteristics**
 $T_J = 25^\circ\text{C}$  unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static characteristics</b>						
Drain-source breakdown voltage	$BV_{DSS}$	$V_{GS}=0\text{ V}, I_D=250\mu\text{A}$	100	---	---	V
Gate threshold voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	2	3	4	V
Drain-source leakage current	$I_{DSS}$	$V_{DS}=100\text{ V}, V_{GS}=0\text{ V}$	---	---	1	$\mu\text{A}$
Gate leakage current, Forward	$I_{GSSF}$	$V_{GS}=20\text{ V}, V_{DS}=0\text{ V}$	---	---	100	nA
Gate leakage current, Reverse	$I_{GSSR}$	$V_{GS}=-20\text{ V}, V_{DS}=0\text{ V}$	---	---	-100	nA
Drain-source on-state resistance	$R_{DS(on)}$	$V_{GS}=10\text{ V}, I_D=13.5\text{ A}$	---	3.8	4.5	m $\Omega$
Forward transconductance	$g_{fs}$	$V_{DS}=10\text{ V}, I_D=20\text{ A}$	---	50	---	S
<b>Dynamic characteristics</b>						
Input capacitance	$C_{iss}$	$V_{DS}=50\text{ V}, V_{GS}=0\text{ V},$ $F=1\text{ MHz}$	---	4725	---	pF
Output capacitance	$C_{oss}$		---	609	---	
Reverse transfer capacitance	$C_{rss}$		---	14	---	
Turn-on delay time	$t_{d(on)}$	$V_{DD}=50\text{ V}, V_{GS}=10\text{ V}, I_D=20\text{ A}$ $R_G=3\Omega$	---	35	---	ns
Rise time	$t_r$		---	18	---	
Turn-off delay time	$t_{d(off)}$		---	45	---	
Fall time	$t_f$		---	55	---	
<b>Gate charge characteristics</b>						
Gate to source charge	$Q_{gs}$	$V_{DS}=50\text{ V}, I_D=20\text{ A},$ $V_{GS}=10\text{ V}$	---	28	---	nC
Gate to drain charge	$Q_{gd}$		---	15	---	
Gate charge total	$Q_g$		---	74	---	
<b>Drain-Source diode characteristics and Maximum Ratings</b>						
Continuous Source Current	$I_S$		---	---	114	A
Pulsed Source Current <sup>3)</sup>	$I_{SM}$		---	---	342	A
Diode Forward Voltage	$V_{SD}$	$V_{GS}=0\text{ V}, I_S=13.5\text{ A}, T_J=25^\circ\text{C}$	---	---	1.3	V
Reverse recovery time	$t_{rr}$	$I_F=13.5\text{ A}, dI_F/dt=100\text{ A}/\mu\text{s}$	---	70	---	ns
Reverse recovery charge	$Q_{rr}$		---	170	---	nC

**Notes:**

1: Repetitive Rating: Pulse width limited by maximum junction temperature.

 2:  $V_{DD}=25\text{ V}, V_{GS}=10\text{ V}, L=0.1\text{ mH}, I_{AS}=12\text{ A},$  Starting  $T_J=25^\circ\text{C}$ .

 3: Pulse Test: Pulse Width  $\leq 300\ \mu\text{s}$ , Duty Cycle  $\leq 2\%$ .

**Electrical Characteristics Diagrams**

Fig 1. Typ. Output Characteristics

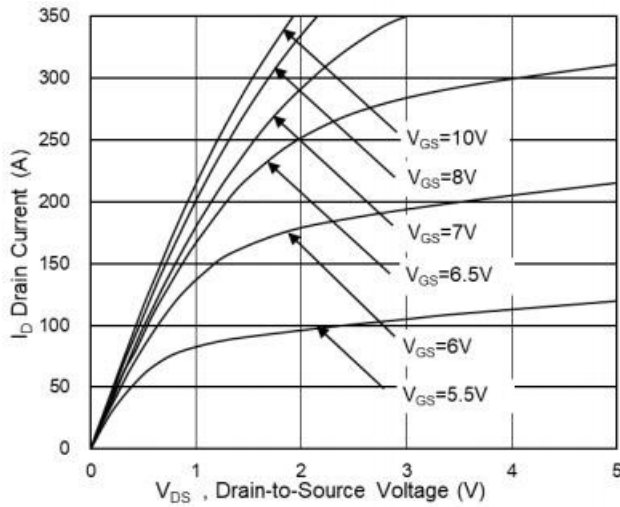


Fig 2. On-Resistance vs G-S Voltage

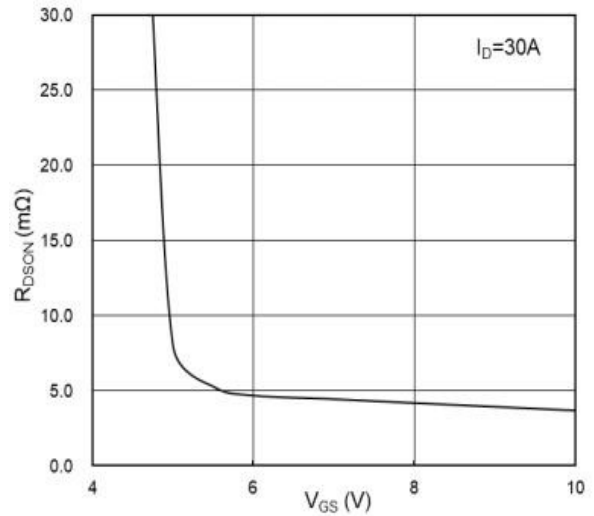


Fig 3. Capacitance Characteristics

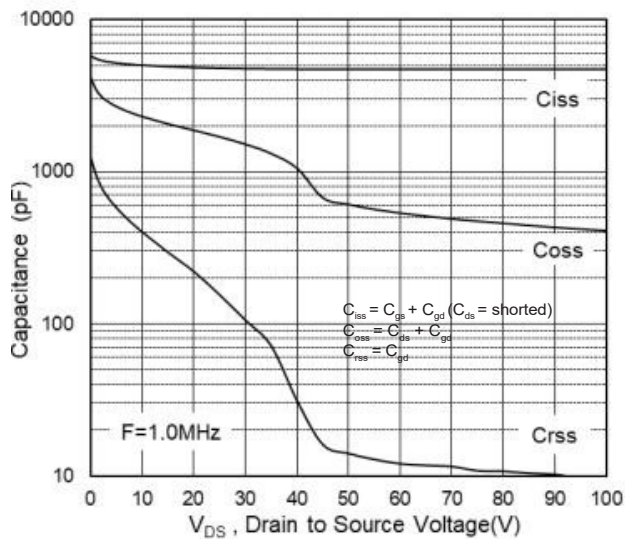


Fig 4. Gate Charge Waveform

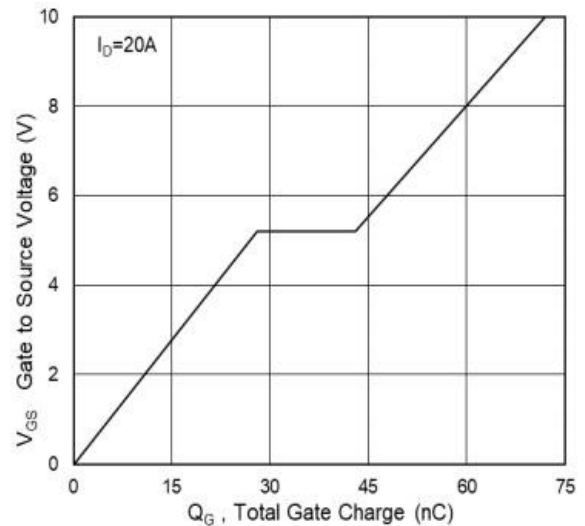


Fig 5. Body-Diode Characteristics

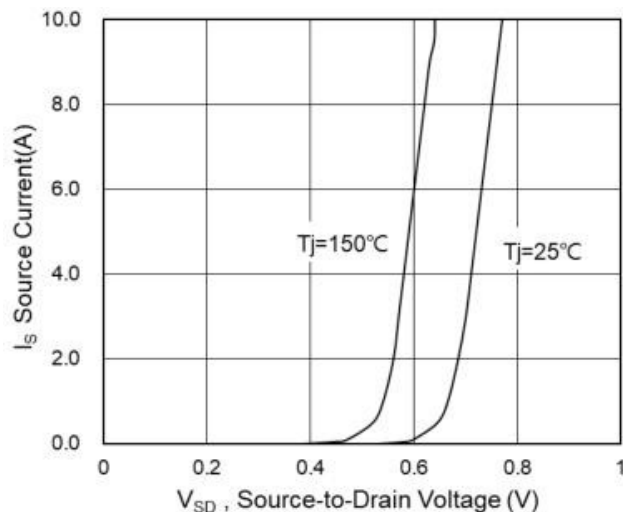


Fig 6. Rds(on)-Junction Temperature

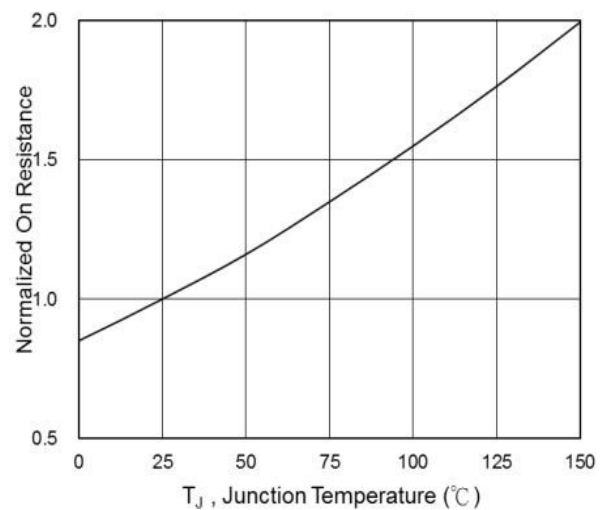


Fig 7.  $V_{GS(th)}$ -Junction Temperature

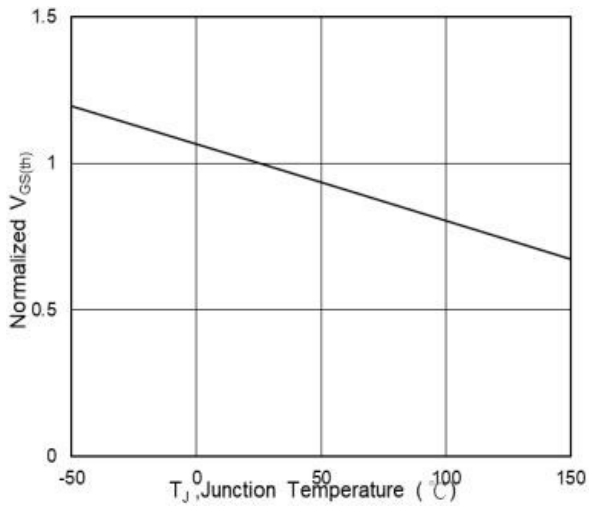


Fig 8: Safe Operating Area

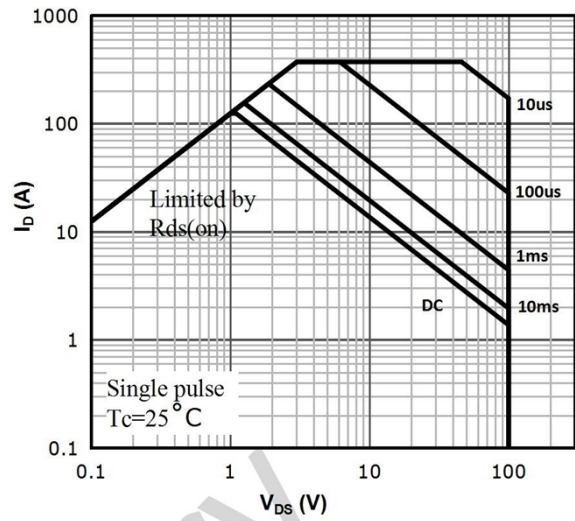
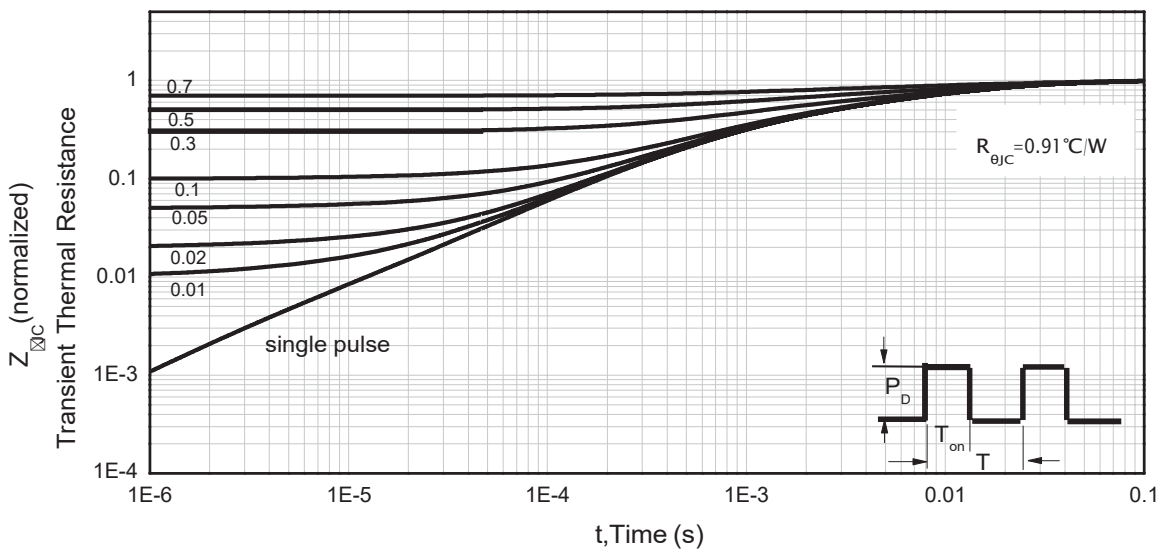
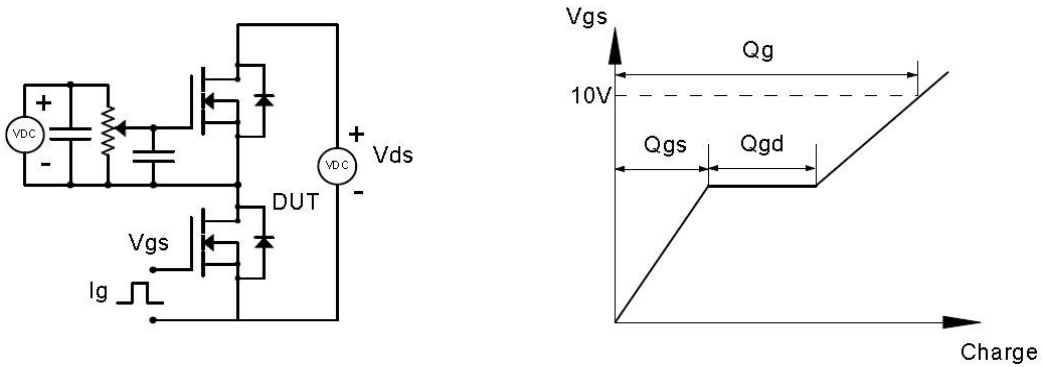
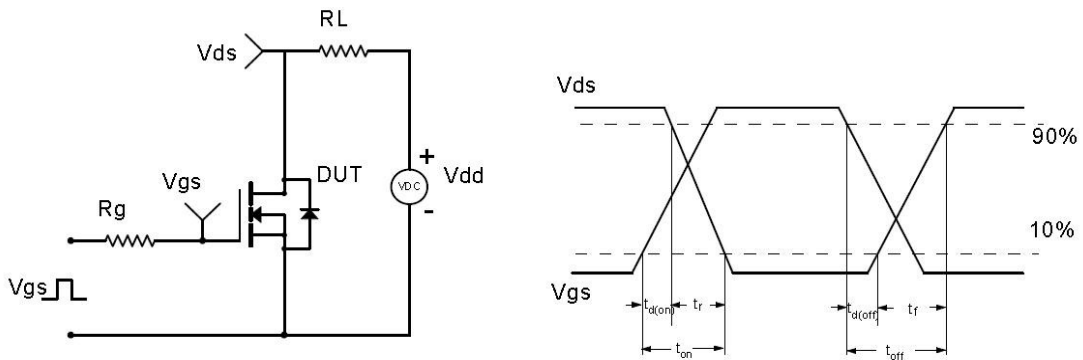
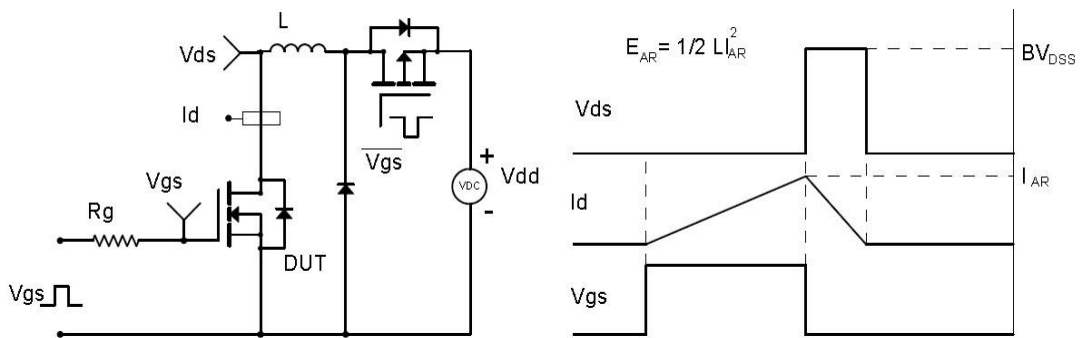


Fig 9. Normalized Maximum Transient Thermal Impedance ( $R_{thJC}$ )



**Test Circuit & Waveform**
**Gate Charge Test Circuit & Waveform**

**Resistive Switching Test Circuit & Waveforms**

**Unclamped Inductive Switching (UIS) Test Circuit & Waveforms**

**Diode Recovery Test Circuit & Waveforms**
