Description

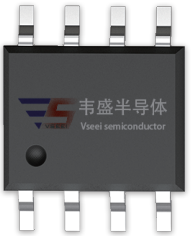
The series of devices uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g. This device is ideal for high-frequency switching and synchronous rectification.

Application

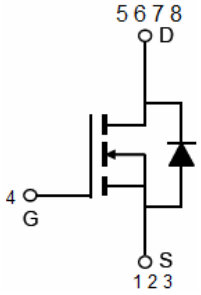
- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

General Features

- V_{DS} =100V,I_D =12A
  $R_{DS(ON)}$=9.1mΩ , typical@ V_{GS}=10V
  $R_{DS(ON)}$=12mΩ , typical@ V_{GS}=4.5V
- Excellent gate charge x $R_{DS(on)}$ product(FOM)
- Very low on-resistance $R_{DS(on)}$
- 150 °C operating temperature
- Pb-free lead plating



SOP-8



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
VST10N091-S8	VST10N091	SOP-8	-	-	-

Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	±20	V
Drain Current-Continuous	I_D	12	A
Drain Current-Continuous(T_C=100℃)	I_D (100℃)	8.5	A
Pulsed Drain Current	I_{DM}	48	A
Maximum Power Dissipation	P_D	3.4	W
Single pulse avalanche energy (Note 4)	E_{AS}	200	mJ
Operating Junction and Storage Temperature Range	T_J,T_{STG}	-55 To 150	℃

Thermal Characteristic

Thermal Resistance,Junction-to-Ambient	$R_{\theta JA}$	37	℃/W

Electrical Characteristics (T_A=25℃unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V I_D=250μA	100		-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=100V,V_{GS}=0V	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	V_{GS}=±20V,V_{DS}=0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250μA	1.1	1.7	2.5	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D=12A	-	9.1	11.0	mΩ
		V_{GS}=4.5V, I_D=12A	-	12.0	16.0	
Forward Transconductance	g_{FS}	V_{DS}=5V,I_D=12A		45	-	S
Dynamic Characteristics (Note3)						
Input Capacitance	C_{lss}	V_{DS}=50V,V_{GS}=0V, F=1.0MHz	-	2600	-	pF
Output Capacitance	C_{oss}		-	230	-	pF
Reverse Transfer Capacitance	C_{rss}		-	27	-	pF
Switching Characteristics (Note 3)						
Turn-on Delay Time	$t_{d(on)}$	V_{DD}=50V,I_D=12A V_{GS}=10V,R_G=1.6Ω	-	13	-	nS
Turn-on Rise Time	t_r		-	10	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	30	-	nS
Turn-Off Fall Time	t_f		-	8	-	nS
Total Gate Charge	Q_g	V_{DS}=50V,I_D=12A, V_{GS}=10V	-	54	-	nC
Gate-Source Charge	Q_{gs}		-	10	-	nC
Gate-Drain Charge	Q_{gd}		-	14	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 2)	V_{SD}	V_{GS}=0V,I_S=12A	-	-	1.2	V
Diode Forward Current	I_S		-	-	12	A
Reverse Recovery Time	t_{rr}	T_J = 25°C, I_F = 12A di/dt = 100A/μs (Note3)	-	55	-	nS
Reverse Recovery Charge	Qrr		-	98	-	nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
3. Guaranteed by design, not subject to production
4. EAS condition : Tj=25℃,V_{DD}=50V,V_G=10V,L=0.25mH,Rg=25Ω

Typical Electrical and Thermal Characteristics

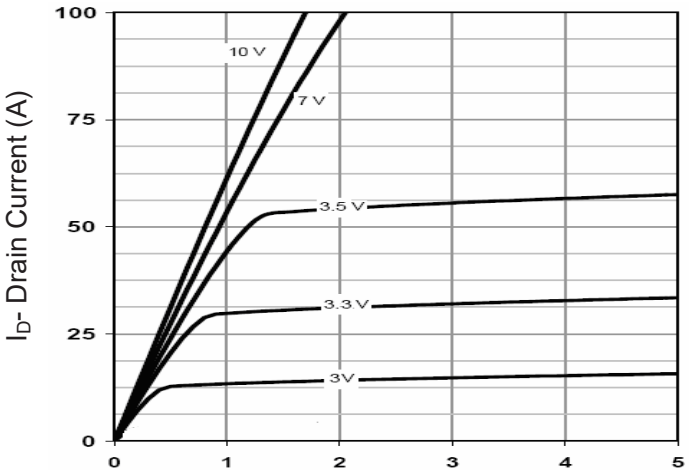


Figure 1 Output Characteristics

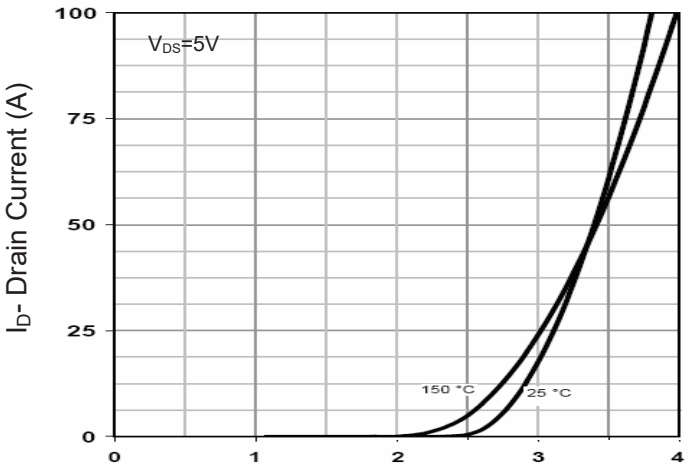


Figure 2 Transfer Characteristics

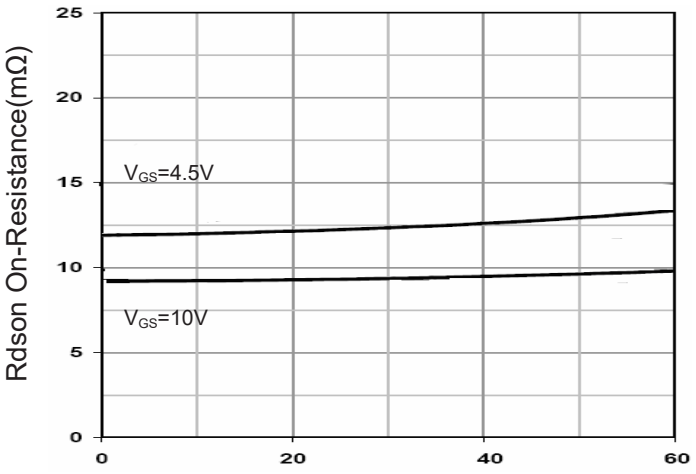


Figure 3 Rdson- Drain Current

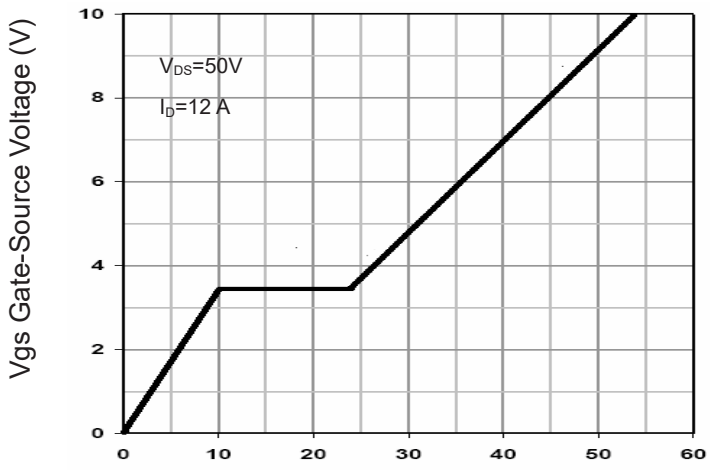


Figure 4 Gate Charge

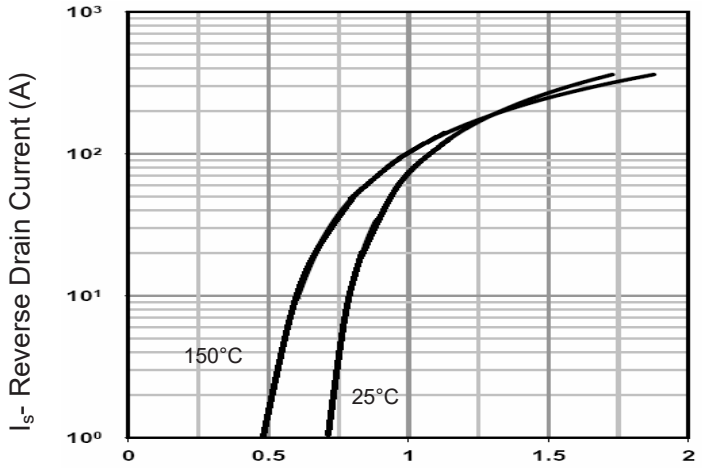


Figure 5 Source- Drain Diode Forward

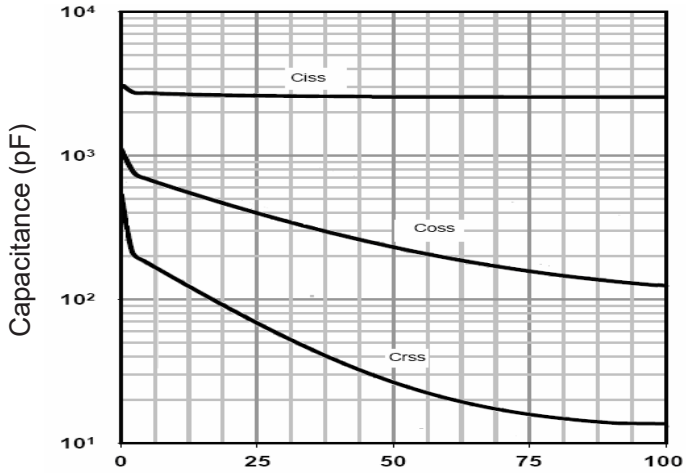


Figure 6 Capacitance vs Vds

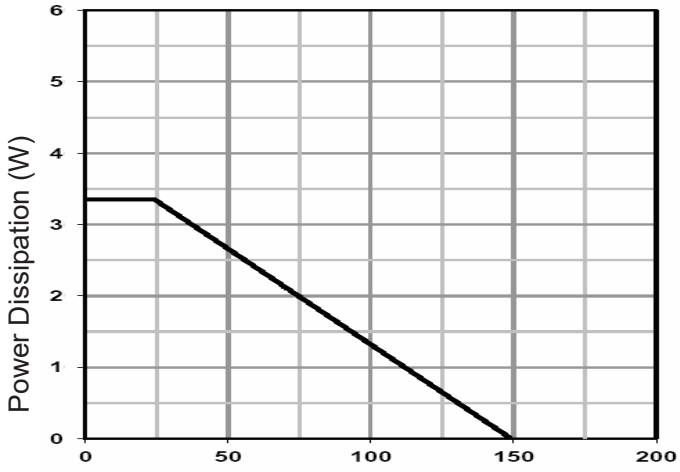


Figure 7 Power De-rating

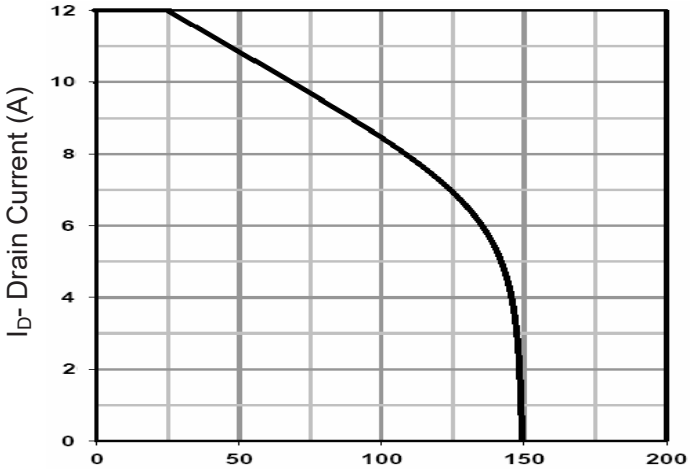


Figure 9 Current De-rating

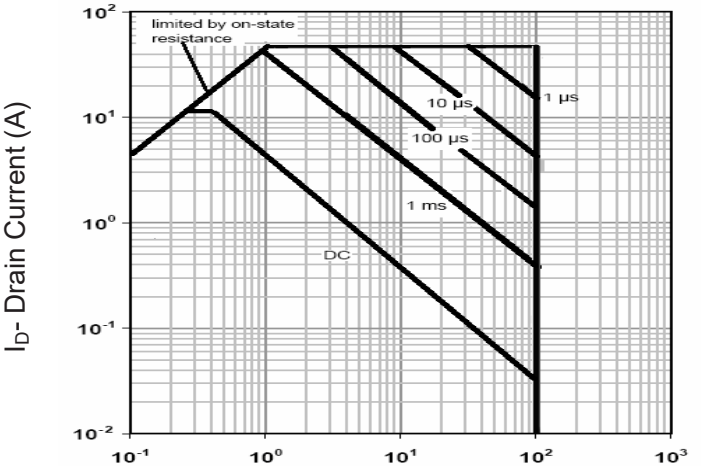


Figure 8 Safe Operation Area

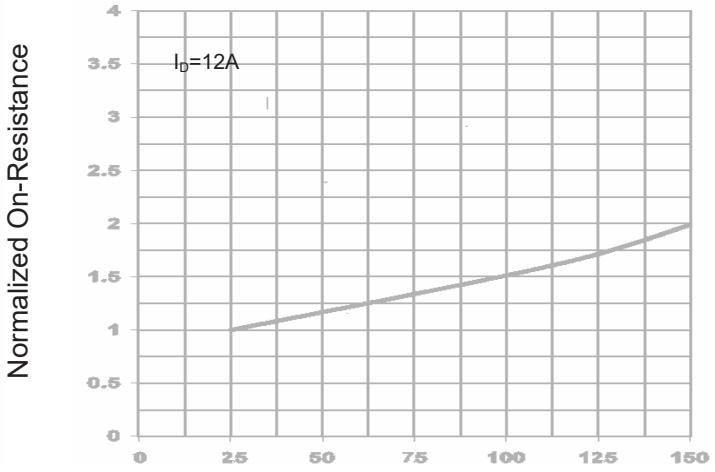


Figure 10 Rdson-Junction Temperature

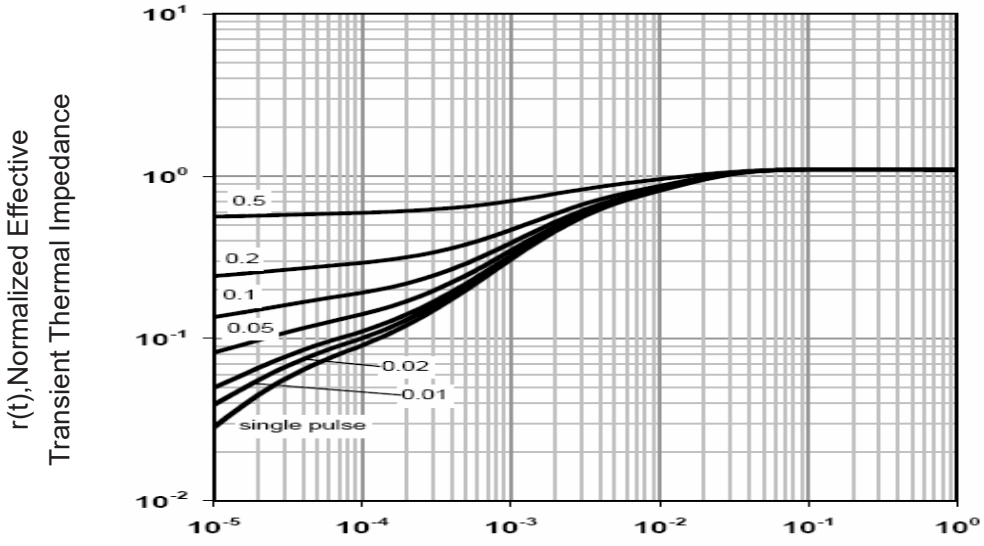


Figure 11 Normalized Maximum Transient Thermal Impedance